Question No: 1 (Marks: 1) - Please choose one
Suppose AL contains 5 decimal then after two left shifts produces the value as

► 5
► 10
► 15
► 20  (Page 52)

Question No: 2 (Marks: 1) - Please choose one
In graphics mode a location in video memory corresponds to a _____________ on the screen.

► line
► dot  (Page 149)
► circle
► rectangle

Question No: 3 (Marks: 1) - Please choose one
Creation of threads can be

► static
► dynamic  (Page 141)
► easy
► difficult

Question No: 4 (Marks: 1) - Please choose one
The thread registration code initializes the PCB and adds it to the linked list so that the __________ will give it a turn.

► assembler
► scheduler  (Page 141)
► linker
► debugger
Question No: 5 (Marks: 1) - Please choose one
VESA VBE 2.0 is a standard for
► High resolution Mode (Page 180)
► Low resolution Mode
► Medium resolution Mode
► Very High resolution Mode

Question No: 6 (Marks: 1) - Please choose one
Which of the following gives the more logical view of the storage medium
► BIOS
► DOS (Page 155)
► Both
► None

Question No: 7 (Marks: 1) - Please choose one
Which of the following IRQs is derived by a key board?
► IRQ 0
► IRQ 1 (Page 114)
► IRQ 2
► IRQ 3

Question No: 8 (Marks: 1) - Please choose one
Which of the following IRQs is used for Floppy disk derive?
► IRQ 4
► IRQ 5
► IRQ 6 (Page 114)
► IRQ 7

Question No: 9 (Marks: 1) - Please choose one
Which of the following pins of a parallel port connector are grounded?
► 10-18
► 18-25 (Page 125)
► 25-32
► 32-39

Question No: 10 (Marks: 1) - Please choose one
The physical address of IDT (Interrupt Descriptor Table) is stored in _______
► GDTR
► IDTR (Page 182)
► IVT
► IDTT
Question No: 11  (Marks: 1) - Please choose one
In NASM an imported symbol is declared with the ............................ while and exported symbol is declared with the............................
► Global directive, External directive
► External directive, Global directive  (Page 189)

Question No: 12  (Marks: 1) - Please choose one
In 68K processors there is a ......................... program counter (PC) that holds the address of currently executing instruction
► 8bit
► 16bit
► 32bit  (Page 191)
► 64bit

Question No: 13  (Marks: 1) - Please choose one
To reserve 8-bits in memory ___ directive is used.
► db  (Page 25)
► dw
► dn
► dd

Question No: 14  (Marks: 1) - Please choose one
In the “mov ax, 5”  5 is the__________ operand.
► source  (Page 18)
► destination
► memory
► register

Question No: 15  (Marks: 1) - Please choose one
RETF will pop the segment address in the
► CS register  (Page 72)
► DS register
► SS register
► ES register

Question No: 16  (Marks: 1) - Please choose one
For the execution of the instruction “DIV  BL”, the implied dividend will be stored in
► AX  (Page 85)
► BX
► CX
► DX
Question No: 17 (Marks: 1) - Please choose one
When a number is divided by zero ”A Division by 0” interrupt is generated. Which instruction is used for this purpose
► INT 0
► INT 1
► INT 2
► This interrupt is generated automatically  (Page 107)

Question No: 18 (Marks: 1) - Please choose one
INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.
► AL  (Page 152)
► BL
► CL
► BH

Question No: 19 (Marks: 1) - Please choose one
BIOS sees the disks as
► logical storage
► raw storage  (Page 155)
► in the form of sectors only
► in the form of tracks only

Question No: 20 (Marks: 1) - Please choose one
In 9pin DB 9, which pin number is assigned to CD (Carrier Detect)?
► 1  (Page 171)
► 2
► 3
► 4

Question No: 21 (Marks: 1) - Please choose one
In 9pin DB 9, Signal ground is assigned on pin number
► 4
► 5  (Page 171)
► 6
► 3

Question No: 22 (Marks: 1) - Please choose one
In 9pin DB 9, RI (Ring Indicator) is assigned on pin number
► 6
► 7
► 8
► 9  (Page 171)
Question No: 23 (Marks: 1) - Please choose one
Motorola 68K processors have...................... 23bit general purpose registers.
► 4
► 8
► 16 (Page 191)
► 32

Question No: 24 (Marks: 1) - Please choose one
When two devices in the system want to use the same IRQ line then what will happen?
► An IRQ Collision
► An IRQ Conflict (Page 114)
► An IRQ Crash
► An IRQ Blockage

Question No: 25 (Marks: 1) - Please choose one
In the instruction MOV AX, 5 the number of operands are
► 1
► 2 (Page 25)
► 3
► 4

Question No: 26 (Marks: 1) - Please choose one
Which flags are NOT used for mathematical operations ?
► Carry, Interrupt and Trap flag.
► Direction, Interrupt and Trap flag. (Page 133)
► Direction, Overflow and Trap flag.
► Direction, Interrupt and Sign flag.

CS401- Computer Architecture and Assembly Language Programming
FINALTERM EXAMINATION
Spring 2010

Question No: 1 (Marks: 1) - Please choose one
The physical address of the stack is obtained by
► SS:SI combination
► SS:SP combination (Page 68)
► ES:BP combination
► ES:SP combination
Question No: 2  (Marks: 1) - Please choose one
Value of AH in the write Graphics pixel service is
► 0Ch (Page 152)
► 0Bh
► 1Ch
► 2Ch

Question No: 3  (Marks: 1) - Please choose one
Threads can have function calls, parameters and __________ variables.
► global
► local (Page 141)
► legal
► illegal

Question No: 4  (Marks: 1) - Please choose one
Creation of threads can be
► static
► dynamic (Page 141)
► easy
► difficult

Question No: 5  (Marks: 1) - Please choose one
How many prevalent calling conventions do exist
► 1
► 2 (Page 187)
► 3
► 4

Question No: 6  (Marks: 1) - Please choose one
VESA VBE 2.0 is a standard for
► High resolution Mode (Page 180)
► Low resolution Mode
► Medium resolution Mode
► Very High resolution Mode

Question No: 7  (Marks: 1) - Please choose one
The serial port connection is a -------------- connector
► 9pin DB 9 (Page 171)
► 8pin DB 9
► 3pin DB 9
► 9pin DB 5
Question No: 8 (Marks: 1) - Please choose one
Which of the following gives the more logical view of the storage medium
► BIOS
► DOS (Page 55) rep
► Both
► None

Question No: 9 (Marks: 1) - Please choose one
In STOSB instruction, when DF is clear, SI is
► Incremented by 1 (Page 92)
► Incremented by 2
► Decremented by 1
► Decremented by 2

Question No: 10 (Marks: 1) - Please choose one
After the execution of STOSW the CX will be
► Decremented by 1 (Page 92)
► Decremented by 2
► Incremented by 1
► Incremented by 2

Question No: 11 (Marks: 1) - Please choose one
IRQ is referred to
► Eight input signals
► One output signal
► One input signals
► Eight output signals

Question No: 12 (Marks: 1) - Please choose one
Which of the following IRQs is derived by a key board?
► IRQ 0
► IRQ 1 (Page 113) rep
► IRQ 2
► IRQ 3

Question No: 13 (Marks: 1) - Please choose one
Which of the following IRQs is connected to serial port COM 1?
► IRQ 4 (page 114)
► IRQ 5
► IRQ 6
► IRQ 7
Question No: 14 (Marks: 1) - Please choose one
The physical address of IDT (Interrupt Descriptor Table) is stored in _______.
- GDTR
- IDTR (Page 182) rep
- IVT
- IDTT

Question No: 15 (Marks: 1) - Please choose one
Assembly language is:
- Low-level programming language (Page 3)
- High-level programming language
- Also known as machine language
- Not considered closer to the computer

Question No: 16 (Marks: 1) - Please choose one
The number of bits required to access 1MB of memory are
- 16 bits
- 32 bits
- Depends on the processor architecture (Page 20)
- 20 bits

Question No: 17 (Marks: 1) - Please choose one
In STOSB instruction, SI is decremented or incremented by
- 3
- 2
- 1 (Page 92)
- 4

Question No: 18 (Marks: 1) - Please choose one
In programmable interrupt controller, which of the following ports is referred as a control port.
- 19
- 20 (Page 115)
- 21
- 22

Question No: 19 (Marks: 1) - Please choose one
INT 21 service 01H is used to read character from standard input with echo. It returns the result in ______ register.
- AL (Page 152) rep
- BL
- CL
- BH
Question No: 20  ( Marks: 1 )  - Please choose one
In device attribute word, which of the following bit decides whether it is a character device or a block device
► Bit 12
► Bit 13
► Bit 14
► Bit 15  (Page 116)

Question No: 21  ( Marks: 1 )  - Please choose one
In 9pin DB 9, which pin number is assigned to CTS (Clear To Send) ?
► 6
► 7
► 8  (Page 171)
► 9

Question No: 22  ( Marks: 1 )  - Please choose one
In 9pin DB 9, which pin number is assigned to RD (Received Data) ?
► 1
► 2  (Page 171)
► 3
► 4

Question No: 23  ( Marks: 1 )  - Please choose one
VESA(Video Electronics Standards Association) organizes 16 color bits for every pixel in
► 5:5:5 format
► 5:6:5 format  (Page 180)
► 6:5:6 format
► 5:6:7 format

Question No: 24  ( Marks: 1 )  - Please choose one
Motorola 68K processors have ..................... 23bit general purpose registers.
► 4
► 8
► 16  (Page 191) rep
► 32

Question No: 25  ( Marks: 1 )  - Please choose one
Programmable Interrupt Controller (PIC) has
► One input signals and eight output signals
► One input signal and one output signal
► Eight input signals and one output signals  (Page 113)
► Eight input signals and eight output signals

Muhammad Moaaz Siddiq MCS (3rd)
mc199401285@gmail.com
Campus: Institute of E-Learning & Modern Studies (IEMS) Samundari
Question No: 26  (Marks: 1) - Please choose one
Video services are classified into........................ broad categories.

► 5
► 4
► 3
► 2 (Page 149)

CS401- Computer Architecture and Assembly Language Programming
FINALTERM EXAMINATION
Spring 2010

1. BL contains 5 decimal then after right shift , BL will become
   • 3
   • 2.5 (Page 52)
   • 5
   • 10

2. 8 * 16 font is stored in ________ bytes.
   • 3
   • 4
   • 8
   • 16 (Page 150)

3. In DOS input buffer , number of characters actually read on return is stored in
   • First byte
   • Second byte  (Page 152)
   • Third byte
   • Fourth byte

4. IRQ 0 has priority
   • Low
   • High
   • Highest (Page 114)
   • Medium
5. Thread registration code initialize PCB and add to linked list so that _____ will give it turn.
   • Assembler
   • Linker
   • Scheduler (Page 141) rep
   • Debugger

6. Traditional calling conventions are in ______ number
   • 1
   • 2 (Page 187)
   • 3
   • 4

7. VESA VEB 2.0 is standard for
   • High Resolution Mode (Page 180) rep
   • Low Resolution Mode
   • Very High Resolution Mode
   • Medium Resolution Mode

8. To clear direction flag which instruction is used
   • Cld (Page 92)
   • Clrd
   • Cl df
   • Clr df

9. In STOSW instruction, When DI is cleared, SI is
   • Incremented by 1
   • Incremented by 2 (Page 92)
   • Decremented by 1
   • Decremented by 2

10. Interrupt that is used in debugging with help of trap flag is
    • INT 0
    • INT 1 (Page 105)
    • INT 2
    • INT 3
11. INT for arithmetic overflow is
   - INT 1
   - INT 2
   - INT 3
   - INT 4 (Page 106)

12. IRQ referred as
   - Eight Input signals (Page 113) rep
   - One Input signal
   - Eight Output signals
   - One output signal

13. IRQ for keyboard is ____1____ (Page 114)

14. IRQ for sound card is ____5____ (Page 114)

15. IRQ for floppy disk is ____6____ (Page 114)

16. IRQ with highest priority is
   - Keyboard IRQ
   - Timer IRQ (Page 114)
   - Sound Card
   - Floppy Disk

17. Pin for parallel port ground is
   - 10-18
   - 18-25 (Page 125) rep
   - 25-32
   - 32-39

18. The physical address of Interrupt Descriptor Table (IDT) is stored in
   - GDTR
   - IDTR (Page 182) rep
   - IVT
   - IDTT

20. CX register is
   - Count register (Page 15)
   - Data register
   - Index register
   - Base register
21. OUT instruction uses __AX or AL__ as source register. (Page 115)

22. IN DB-9 connector the Data Set ready pin is at
   • 5
   • 6 (Page 171)
   • 7
   • 8

23. If two devices uses same IRQ then there is
   • IRQ collision
   • IRQ conflict (Page 114) rep
   • IRQ drop

24. VESA organizes 16 bit color for every pixel in ratio
   • 5:5:5
   • 5:6:5 (Page 180) rep
   • 6:5:6
   • 5:6:7

---

CS401- Computer Architecture and Assembly Language Programming
FINALTERM EXAMINATION
Spring 2010

Question No: 1 (Marks: 1) - Please choose one
SP is associated with............. By default
   ► SS (Page 34)
   ► DS
   ► CS
   ► ES

Question No: 2 (Marks: 1) - Please choose one
Which bit of the attributes byte represents the red component of foreground color
   ► 5
   ► 4
   ► 3
   ► 2 (Page 81)
Question No: 3  (Marks: 1) - Please choose one
An 8 x 16 font is stored in ______________ bytes.
► 2
► 4
► 8
► 16 (Page 150)  rep

Question No: 4  (Marks: 1) - Please choose one
In DOS input buffer, the number of characters actually read on return is stored in __________ byte.
► third
► fourth
► first
► second  (Page 152)  rep

Question No: 5  (Marks: 1) - Please choose one
Which of the following gives the more logical view of the storage medium
► BIOS
► DOS
► Both
► None

Question No: 6  (Marks: 1) - Please choose one
In STOSW instruction, when DF is clear, SI is
► Incremented by 1
► Incremented by 2  (Page 92)
► Decremented by 1
► Decremented by 2

Question No: 7  (Marks: 1) - Please choose one
Which of the following interrupts is Non maskable interrupt
► INT 2  (Page 105)
► INT 3
► INT 0
► INT 1

Question No: 8  (Marks: 1) - Please choose one
Which of the following IRQs is connected to serial port COM 2?
► IRQ 0
► IRQ 1
► IRQ 2
► IRQ 3  (Page 114)
Question No: 9  (Marks: 1)  - Please choose one
The time interval between two timer ticks is?

► 40ms
► 45ms
► 50ms
► 55ms  (Page 122)

Question No: 10  (Marks: 1)  - Please choose one
The physical address of IDT (Interrupt Descriptor Table) is stored in ______

► GDTR
► IDTR  (Page 182) rep
► IVT
► IDTT

Question No: 11  (Marks: 1)  - Please choose one
In NASM an imported symbol is declared with the ......................... while and exported symbol is declared with the.........................

► Global directive, External directive
► External directive, Global directive  (Page 189) rep

Question No: 12  (Marks: 1)  - Please choose one
In 68K processors there is a 32bit ..................... that holds the address of currently executing instruction

► Program counter  (Page 191)
► Stack pointer
► Register
► Stack

Question No: 13  (Marks: 1)  - Please choose one
Single step interrupt is

► Hardware interrupt
► Like divide by zero interrupt  (Page 133)
► Like divide by 1 interrupt
► Software interrupt

Question No: 14  (Marks: 1)  - Please choose one
Which of the following is NOT true about registers:

► Their operation is very much like memory
► Intermediate results may also be stored in registers
► They are also called scratch pad ram
► None of given options
Question No: 15  (Marks: 1) - Please choose one
Types of jump are:
► short, near
► short, near, far  (Page 45)
► near, far
► short, far

Question No: 16  (Marks: 1) - Please choose one
MS DOS uses ____ display mode.
► Character based  (Page 79)
► Graphics based
► Numeric based
► Console based

Question No: 17  (Marks: 1) - Please choose one
Which of the following IRQs is derived by a timer device?
► IRQ 0  (Page 114)
► IRQ 1
► IRQ 2
► IRQ 3

Question No: 18  (Marks: 1) - Please choose one
In programmable interrupt controller, which of the following ports is referred as a control port?
► 19
► 20  (Page 115) rep
► 21
► 22

Question No: 19  (Marks: 1) - Please choose one
INT 21 service 01H is used to read character from standard input with echo. It returns the result in ______ register.
► AL  (Page 152) rep
► BL
► CL
► BH

Question No: 20  (Marks: 1) - Please choose one
In 9pin DB 9, which pin number is assigned to DSR (DataSet Ready) ?
► 4
► 5
► 6  (Page 171)
► 7
Question No: 21 (Marks: 1) - Please choose one
In 9pin DB 9, which pin number is assigned to TD (Transmitted Data)?
- 1
- 2
- 3 (Page 171)
- 4

Question No: 22 (Marks: 1) - Please choose one
In 9pin DB 9, Signal ground is assigned on pin number
- 4
- 5 (Page 171)
- 6
- 3

Question No: 23 (Marks: 1) - Please choose one
8088 is a ....................
- 16 bit processor (Page 14)
- 32 bit processor
- 64 bit processor
- 128 bit processor

Question No: 24 (Marks: 1) - Please choose one
The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).
- 1
- 0 (Page 175)
- -1
- -2

Question No: 25 (Marks: 1) - Please choose one
VESAs (Medical Electronics Standards Association) organizes 16 color bits for every pixel in
- 5:5:5 format
- 5:6:5 format (Page 180) rep
- 6:5:6 format
- 5:6:7 format

Question No: 26 (Marks: 1) - Please choose one
Which flags are NOT used for mathematical operations?
- Carry, Interrupt and Trap flag.
- Direction, Interrupt and Trap flag. (Page 133) rep
- Direction, Overflow and Trap flag.
- Direction, Interrupt and Sign flag.
Question No: 1 (Marks: 1) - Please choose one
When a 32 bit number is divided by a 16 bit number, the quotient is of

$ 32$ bits
$ 16$ bits (Page 85)
$ 8$ bits
$ 4$ bits

Question No: 2 (Marks: 1) - Please choose one
In the instruction MOV AX, 5 the number of operands are

$ 1$
$ 2$ (Page 25) rep
$ 3$
$ 4$

Question No: 3 (Marks: 1) - Please choose one
3. In DOS input buffer, number of characters actually read on return is stored in

$ $ First byte
$ $ Second byte (Page 152) rep
$ $ Third byte
$ $ Fourth byte

Question No: 4 (Marks: 1) - Please choose one
7. VESA VEB 2.0 is standard for

$ $ High Resolution Mode (Page 180) rep
$ $ Low Resolution Mode
$ $ Very High Resolution Mode
$ $ Medium Resolution Mode

Question No: 5 (Marks: 1) - Please choose one
22. IN DB-9 connector the Data Set ready pin is at

$ 5$
$ 6$ (Page 171) rep
$ 7$
$ 8$
Question No: 6 (Marks: 1) - Please choose one
Threads can have function calls, parameters and _____________ variables.
$  global
  $ local  (Page 141)
  $ legal
  $ illegal

Question No: 7 (Marks: 1) - Please choose one
How many prevalent calling conventions do exist
$ 1
$ 2 (Page 187) rep
$ 3
$ 4

Question No: 8 (Marks: 1) - Please choose one
In 9pin DB 9 DSR is assigned on pin number
$ 4
$ 5
$ 6 (Page 171)
$ 7

Question No: 9 (Marks: 1) - Please choose one
In 9pin DB 9 CTS is assigned on pin number
$ 6
$ 7
$ 8 (Page 171)
$ 9

Question No: 10 (Marks: 1) - Please choose one
In 9pin DB 9 CD is assigned on pin number
$ 1 (Page 171)
$ 2
$ 3
$ 4

Question No: 11 (Marks: 1) - Please choose one
A 32bit address register can access upto __________ memory so memory increased a lot.
$2GB
$4GB  (Page 175)
$6GB
$8GB
Question No: 12 (Marks: 1) - Please choose one
in device attribute word which of the following bit decides whether it is a charater device or a block device
$ Bit 12
$ Bit 13
$ Bit 14
$ Bit 15 (Page 166) rep

Question No: 13 (Marks: 1) - Please choose one
9. Which of the following IRQ is cascading interrupt
$ IRQ 0
$ IRQ 1
$ IRQ 2 (Page 114)
$ IRQ 3

Question No: 14 (Marks: 1) - Please choose one
Which of the following interrupts is used for Arithmetic overflow
$ INT 1
$ INT 2
$ INT 3
$ INT 4 (Page 106) rep

Question No: 15 (Marks: 1) - Please choose one
An End of Interrupt (EOI) signal is sent by
$ Handler (Page 114)
$ Processor
$ IRQ
$ PIC

Question No: 16 (Marks: 1) - Please choose one
The number of pins in a parallel port connector are?
$ 20
$ 25 (Page 125)
$ 30
$ 35

Question No: 17 (Marks: 1) - Please choose one
Which of the following pins of a parallel port connector are grounded?
$ 10-18
$ 18-25 (Page 125) rep
$ 25-32
$ 32-39
Question No: 18 (Marks: 1) - Please choose one
In NASM an imported symbol is declared with the ............... while an exported symbol is declared with the......................

$ Global directive, External directive
$ External directive, Global directive (Page 189) rep

Question No: 1 (Marks: 1) - Please choose one
Which of the following is not true about registers:

- Their operation is very much like memory
- Intermediate results may also be stored in registers.
- They are also called scratch pad ram
- None of given options.

Question No: 2 (Marks: 1) - Please choose one
move [bp], al moves the one byte content of the AL register to the address contained in BP register in the current

Stack segment (Page 35)
Code segment
Data segment
Extra segment

Question No: 3 (Marks: 1) - Please choose one
In a rotate through carry right (RCR) instruction applied on a 16 bit word effectively there is

- 16 bits rotation
- 1 bit rotation
- **17 bits rotation** (Page 53)
- 8 bits rotation

Question No: 4 (Marks: 1) - Please choose one
The 8088 stack works on

Word sized elements (Page 68)
Byte sized elements
**Question No: 5 (Marks: 1) - Please choose one**
An 8 x 16 font is stored in ______________ bytes.
2
4
8
16  (Page 150)  rep

**Question No: 6 (Marks: 1) - Please choose one**
INT 10 is used for __________ services.
RAM
Disk
BIOS video   (Page 149)
DOS video

**Question No: 7 (Marks: 1) - Please choose one**
Priority of IRQ 0 interrupt is
medium
high
highest   (Page 114)  rep
low

**Question No: 8 (Marks: 1) - Please choose one**
Threads can have function calls, parameters and __________ variables.
global
local   (Page 141)  rep
legal
illegal

**Question No: 9 (Marks: 1) - Please choose one**
How many prevalent calling conventions do exist
1
2  (Page 187)  rep
3
4

**Question No: 10 (Marks: 1) - Please choose one**
In 9pin DB 9 DSR is assigned on pin number
4
5
6  (Page 171)  rep
7
Question No: 11 (Marks: 1) - Please choose one
In 9pin DB 9 CTS is assigned on pin number
6
7
8 (Page 171) rep
9

Question No: 12 (Marks: 1) - Please choose one
In 9pin DB 9 CD is assigned on pin number
1 (Page 171) rep
2
3
4

Question No: 13 (Marks: 1) - Please choose one
In 9pin DB 9 RD is assigned on pin number
1
2 (Page 171)
3
4

Question No: 14 (Marks: 1) - Please choose one
in device attribute word which of the following bit decides whether it is a character device or a block device
Bit 12
Bit 13
Bit 14
Bit 15 (Page 166)

Question No: 15 (Marks: 1) - Please choose one
Video services are classified into __________ broad categories
2 (Page 149)
3
4
5

Question No: 16 (Marks: 1) - Please choose one
In STOSB instruction, when DF is clear, SI is
Incremented by 1 (Page 92) rep
Incremented by 2
Decremented by 1
Decremented by 2
Question No: 17 (Marks: 1) - Please choose one
The process of sending signals back and forth is called
- Activity
- Hand-shaking
- Interruption
- **Time clicking**

Question No: 18 (Marks: 1) - Please choose one
Which of the following interrupts is used for Arithmetic Overflow?
- INT 1
- INT 2
- INT 3
- **INT 4**  (Page 106) rep

Question No: 19 (Marks: 1) - Please choose one
Which of the following is a special type of interrupt that returns to the same instruction instead of the next instruction?
- Divide overflow interrupt  (Page 107)
- Debug interrupt
- Arithmetic overflow interrupt
- Change of sign interrupt

Question No: 20 (Marks: 1) - Please choose one
Which of the following IRQs is derived by a timer device?
- **IRQ 0**  (Page 114) rep
- IRQ 1
- IRQ 2
- IRQ 3

Question No: 21 (Marks: 1) - Please choose one
Which of the following IRQs is connected to serial port COM 2?
- IRQ 0
- IRQ 1
- IRQ 2
- **IRQ 3**  (Page 114) rep

Question No: 22 (Marks: 1) - Please choose one
An End of Interrupt (EOI) signal is sent by
- **Handler**  (Page 114) rep
- Processor
- IRQ
- PIC
Question No: 23 (Marks: 1) - Please choose one
The source registers in OUT is
- **AL or AX** (Page 115) *rep*
- BL or BX
- CL or CX
- DL or DX

Question No: 24 (Marks: 1) - Please choose one
In programmable interrupt controller which of the following ports is used for selectively enabling or disabling interrupts
- 19
- 20
- **21** (Page 115)
- 22

Question No: 25 (Marks: 1) - Please choose one
The number of pins in a parallel port connector are?
- 20
- **25** (Page 125) *rep*
- 30
- 35

Question No: 26 (Marks: 1) - Please choose one
Which of the following pins of a parallel port connector are grounded?
- 10-18
- **18-25** (Page 125) *rep*
- 25-32
- 32-39

Question No: 27 (Marks: 1) - Please choose one
If the decimal number “35” is shifted by two bit to left, the new value will be
- 35
- 70
- **140** (*00100011 = 35, 10001100 = 140*)
- 17

Question No: 28 (Marks: 1) - Please choose one
A 32bit address register can access upto................ of memory so memory access has increased a lot.
- 2GB
- **4GB** (Page 175) *rep*
- 6GB
- 8GB
Question No: 29 (Marks: 1) - Please choose one
In NASM an imported symbol is declared with the ......................... while and exported symbol is declared with the .........................
   Global directive, External directive
   **External directive, Global directive**  (Page 189) rep

Question No: 30 (Marks: 1) - Please choose one
Single step interrupt is
   Hardware interrupt
   **Like divide by zero interrupt**  (Page 133) rep
   Like divide by 1 interrupt
   Software interrupt
Question No: 4 (Marks: 1) - Please choose one
The value of a segment register when the processor is running under protected mode is called ________
? segment descriptor
? segment selector
? global descriptor table
? protected register

Question No: 5 (Marks: 1) - Please choose one
FS and FS and GS are two ________________ in protected mode.
? segment registers
? segment selectors (Page 175)
? stack pointers
? register pointers

Question No: 6 (Marks: 1) - Please choose one
IRQ 0 interrupt have ________________ priority
? low
? medium
? highest (Page 114) rep
? lowest

Question No: 7 (Marks: 1) - Please choose one
IDT stands for ________________.
? interrupt descriptor table (Page 182)
? individual descriptor table
? inline data table
? interrupt descriptor table

Question No: 8 (Marks: 1) - Please choose one
Every bit of line status in serial port conveys ____________ information.
? different (Page 171)
? same
? partial
? full

Question No: 9 (Marks: 1) - Please choose one
There are total _____________ bytes in a standard floppy disk.
? 1444k
? 1440k (Page 155)
? 1280k
? 2480k
Question No: 10 (Marks: 1) - Please choose one
An 8x16 font is stored in ________________ bytes.
? 8  
? 16 (Page 150) rep  
? 4  
? 20

Cs401 – Quiz No.3 (June 2012)

Question No: 1 (Marks: 1)
Memory address space is selected when which of the following instructions is given to the processor?s
Select correct option:
MOV (page 115)
DEC
IN
ADD

Question No: 2 (Marks: 1)
PCB stands for?
Select correct option:
Process Control Block (Page 140)
Process Clearing Block
Programmable Counter Block
Programs Control Block

Question No: 3 (Marks: 1)
The input frequency of the programmable interval timer (PIT) is
Fixed (Page 122)
Depends on processor clock
Variable
Depends on hardware attached

Question No: 4 (Marks: 1)
Programmable interrupt controller has two ports 20 and 21. Port 20 is the control port while port 21 is.........
The Interrupt mask register (Page 115)
Interrupt port
Output port
Input port
Question No: 5 (Marks: 1)
The time interval between two timer ticks is?
40ms
45ms
50ms
55ms (Page 122)

Question No: 6 (Marks: 1)
Which of the following interrupts is used for saving and restoring the registers
INT6
Int7
Int8 (Page 141)
Int0

Question No: 7 (Marks: 1)
In programmable interrupt controller which of the following ports is referred as a interrupt mask register?
19
20
21 (Page 115)
22

Question No: 8 (Marks: 1)
The programmable interval timer (PIT) has input frequency of
1.193 MHZ (Page 122)
2.192 MHZ
3.196 MHZ
4.198 MHZ

Question No: 9 (Marks: 1)
Peripheral address space is selected when which of the following instructions is given to the processor?
MOV
IN (Page 155)
Add
Out

Question No: 10 (Marks: 1)
Direction flag, the interrupt flag, and the trap flag are
used for mathematical operations
not used for mathematical operations (Page 133)
status flags
not status flags
Question No: 1 (Marks: 1)
The space where all the registers of a task are stored is called the
control block (Page 140)
process control block
stack
memory

Question No: 2 (Marks: 1)
Which of the following interrupt is of highest priority interrupt?
Select correct option:
Keyboard interrupt
Timer interrupt (Page 122)
INT 2
INT 3

Question No: 3 (Marks: 1)
The instruction used to read a character from the keyboard port is
in al, 0x60 (Page 125)
out al, 0x60
in al, 0x80
out al, 0x80

Question No: 4 (Marks: 1)
Each thread can have their own
execution area
stack (Page 141)
memory
array

Question No: 5 (Marks: 1)
All the registers & stack are saved in
Multitasking
multi-processing
function call
BIOS
Question No: 6 (Marks: 1)
There is no instruction to clear the
Select correct option:
* Trap flag (Page 133)
* Interrupt flag
* Direction flag
* None of the above

Question No: 7 (Marks: 1)
The interrupt mask register which can be used for selectively enabling or disabling interrupts is associated with
Select correct option:
* Port 19
* Port 20
* Port 21 (Page 115)
* Port 22

Question No: 8 (Marks: 1)
The parallel port connector is called?
Select correct option:
* DB-25 (Page 125)
* BD-25
* DB-24
* BD-24