Question No: 1  (Marks: 1) - Please choose one
DMA stands for________
► Direct Memory Access  (Page 4)
► Distinct Memory Access
► Direct Module Access
► Direct Memory Allocation

Question No: 2  (Marks: 1) - Please choose one
Standard PC operates in two modes in terms of memory which are
► Real mode and Extended Mode
► Base mode and Memory Mode
► None of the given
► Real mode and protected mode  (Page 6)

Question No: 3  (Marks: 1) - Please choose one
UART stands for_______
► Universal Asynchronous Receiver Transmitter  (Page 107)
► Universal Adjustment and Realigning Tool
► Unconventional Assisted Recovery Team
► None of these

Question No: 4  (Marks: 1) - Please choose one
Usually interrupt procedures are reentrant procedures especially those interrupt procedure compiled using C language compiler are reentrant.
► True  (Page 38)
► False

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Question No: 5 (Marks: 1) - Please choose one
DTE is ____________.

► Data terminal equipment (Page 109)
► Data transmitting equipment
► Dual terminal equipment
► None of the given.

Question No: 6 (Marks: 1) - Please choose one
Interrupt Vector Table (IVT) in short is a ______ bytes sized table.

► 1024 (Page 10)
► 2048
► 3072
► 4096

Question No: 7 (Marks: 1) - Please choose one
Hardware Interrupts are ____________.

► Preemptive
► Non-Preemptive (Page 48)
► Both Preemptive and Non-Preemptive
► None of Given

Question No: 8 (Marks: 1) - Please choose one
Timer interrupt is a ________.

► Hardware Interrupt (Page 28)
► Software Interrupt
► Both of these
► None of These

Question No: 9 (Marks: 1) - Please choose one
Int 14H ____________ can be used to set the line parameter of the UART or COM port.

► Service # 0 (Page 119)
► Service # 1
► Service # 2
► None of the given options
Question No: 10  (Marks: 1) - Please choose one
The keyboard makes use of interrupt number _______ for its input operations.
► 9  (Page 34)
► 10
► 11
► 12

Question No: 11  (Marks: 1) - Please choose one
In self test mode the output of the UART is routed to its input.
► True  (Page 117)
► False

Question No: 12  (Marks: 1) - Please choose one
In case of synchronous communication a timing signal is required to identify the start and end of a bit.
► True  (Page 105)
► False

Question No: 13  (Marks: 1) - Please choose one
Register can be used to divide frequency is _______
► Counter Register  (Page 69)
► Accumulator Register
► None of these

Question No: 14  (Marks: 1) - Please choose one
Only _______ ports are important from programming point of view.
► 70 and 71H  (Page 141)
► 71 and 72H
► 70 and 72H
► 72 and 73H

Question No: 15  (Marks: 1) - Please choose one
Which port is known as Data Port_____
► 60H  (Page 177)
► 61H
► 64H
► 69H
Question No: 16  (Marks: 1) - Please choose one
LPTs can be swapped.
► True (Page 92)
► False

Question No: 17  (Marks: 1) - Please choose one
PPI is used to perform parallel communication
► True (Page 81)
► False

Question No: 18  (Marks: 1) - Please choose one
__________ is used to control the printer via the BIOS
► Int 16H
► Int 17H (Page 84)
► Int 18H
► Int 19H

Question No: 19  (Marks: 1) - Please choose one
_____ bit is cleared to indicate the low nibble is being sent.
► D1
► D2
► D3
► D4 (Page 104)

Question No: 20  (Marks: 1) - Please choose one
DSR stands for __________ .
► Data set ready (Page 111)
► Data service ready
► Data stock ready
► None of the given
Question No: 1  (Marks: 1) - Please choose one
There are two main types of interrupts namely _________________.

- PC based and Window based
- Hardware based and Kernel based
- **Hardware interrupts and Software interrupts** (Page 10)
- None of the given

Question No: 2  (Marks: 1) - Please choose one
To set the interrupt vector means is to change the double word sized interrupt vector within the IVT.

- **True** (Page 22)
- False

Question No: 3  (Marks: 1) - Please choose one
The service number is usually placed in the ________ register.

- AL
- CL
- **AH** (Page 26)
- AX

Question No: 4  (Marks: 1) - Please choose one
The keyboard makes use of interrupt number _______ for its input operations.

- **9** (Page 34)
- 10
- 11
- 12

Question No: 5  (Marks: 1) - Please choose one
The service __________ is called the keyboard hook service.

- **15H/2FH** (Page 44)
- 15H/4FH
- 15H/FFH
Question No: 6  (Marks: 1) - Please choose one
The BIOS interrupt ________ can be used to configure RTC.

► 1AH  (Page 136)
► 2AH
► 3AH
► 4AH

Question No: 7  (Marks: 1) - Please choose one
The interval timer can operate in ________ modes.

► Five
► Seven
► Four
► Six  (Page 72)

Question No: 8  (Marks: 1) - Please choose one
______ is Disk interrupt.

► 10H
► 11H
► 13H  (Page 42)
► 14H

Question No: 9  (Marks: 1) - Please choose one
PPI stands for
► Parallel Programmable interface
► Peripheral Programmable interface  (Page 76)
► Port Programmable interface
► None of the given

Question No: 10  (Marks: 1) - Please choose one
Int ________ is used to control the printer via the BIOS.

► 17H  (Page 84)
► 18H
► 20H
► 21H

Question No: 11  (Marks: 1) - Please choose one
Counter register can be used to divide clock signal.
► True  (Page 69)
► False
Question No: 12  (Marks: 1) - Please choose one
DCE stands for __________.

► Data communication equipment  (Page 109)
► Distributed Computing Environment
► Data Communications Equipment
► Data Carrier Equipment

Question No: 13  (Marks: 1) - Please choose one
__________ is used to identify the cause of interrupt.

► Interrupt ID Register  (Page 116)
► PC Register
► AC Register
► None of All These

Question No: 14  (Marks: 1) - Please choose one
The bit number _______ of the coprocessor control word is the interrupt enable flag.

► 7  (Page 168)
► 8
► 9
► 6

Question No: 15  (Marks: 1) - Please choose one
There are __________ kinds of serial communication.

► 2  (Page 105)
► 3
► 4
► 5

Question No: 16  (Marks: 1) - Please choose one
__________ store the base address for LPT1.

► 40:00H
► 40:02H
► 40:08H  (Page 92)
► 40:1AH
Question No: 17  (Marks: 1) - Please choose one
The amount of memory above conventional memory (extended memory) can be determined using the service _______.
► 15H/88H  (Page 162)
► 16H/88H
► 17H/88H
► 21H/88H

Question No: 18  (Marks: 1) - Please choose one
The output on the monitor is controlled by a controller called __________ within the PC.
► Video controller  (Page 30)
► Bus controller
► Ram controller
► None of the given

Question No: 19  (Marks: 1) - Please choose one
The keyboard input character scan code is received at ___ port.
► 60H  (Page 179)
► 61H
► 62H
► 63H

Question No: 20  (Marks: 1) - Please choose one
_______ is LED control byte.
► 0xFD
► 0xED  (Page 181)
► 0xFF
► 0xEE
Question No: 1  (Marks: 1) - Please choose one
There are two main types of interrupts, namely

► PC based and Window based  
► Hardware based and Kernel based  
► **Hardware interrupts and Software interrupts** (Page 10)  
► None of the given

Question No: 2  (Marks: 1) - Please choose one
Standard PC operates in two modes in terms of memory which are

► Real mode and Extended Mode  
► Base mode and Memory Mode  
► None of the given  
► **Real mode and protected mode** (Page 6)  

Question No: 3  (Marks: 1) - Please choose one
BPB stands for ____________.

► **BIOS parameter block** (Page 243)  
► BIOS processing block  
► Base processing block  
► BIOS partition block

Question No: 4  (Marks: 1) - Please choose one
The Function of I/O controller is to provide ____________.

► I/O control signals  
► Buffering  
► Error Correction and Detection  
► **All of given** (Page 5)

Question No: 5  (Marks: 1) - Please choose one
IVT is a table containing _____ byte entries each of which is a far address of an interrupt service routine.

► 2  
► 4 (Page 20)  
► 8  
► 16
Question No: 6  (Marks: 1) - Please choose one
Each paragraph in keep function is ____ bytes in size.
► 4
► 8
► 16 (Page 24)
► 32

Question No: 7  (Marks: 1) - Please choose one
Interrupt 9 usually reads the _________ from keyboard.
► ASCII code
► Scan code  (Page 34)
► Both ASCII and Scan code
► None of the above

Question No: 8  (Marks: 1) - Please choose one
A software interrupt does not require EOI (End of interrupt).
► True (Page 49)
► False

Question No: 9  (Marks: 1) - Please choose one
To store each character in keyboard buffer ____ bytes are required.
► 2 (Page 54)
► 4
► 6
► 8

Question No: 10  (Marks: 1) - Please choose one
Interrupt ____ is empty; we can use its vector as a flag.
► 9H
► 13H
► 15H
► 65H (Page 65)

Question No: 11  (Marks: 1) - Please choose one
Command register is an ____ bit register
► 4
► 8 (Page 71)
► 16
► 32
Question No: 12  (Marks: 1) - Please choose one
The interval timer can operate in ____ modes.

► Three
► Four
► Five
► Six  (Page 72)  rep

Question No: 13  (Marks: 1) - Please choose one
The following command “outportb (0x61,inportb(0x61) | 3);” will ________.
► Turn on the speaker  (Page 74)
► Turn off the speaker
► Toggle the speaker
► None of the above

Question No: 14  (Marks: 1) - Please choose one
The PPI acts as an interface between the CPU and a parallel ________.
► I/O device  (Page 83)
► CPU
► BUS
► None of Given

Question No: 15  (Marks: 1) - Please choose one
DTE is ____________.
► Data terminal equipment  (Page 109)  rep
► Data transmitting equipment
► Dual terminal equipment
► None of the given.

Question No: 16  (Marks: 1) - Please choose one
DSR stands for ____________.
► Data set ready  (Page 111)  rep
► Data service ready
► Data stock ready
► None of the given

Question No: 17  (Marks: 1) - Please choose one
In self test mode the output of the UART is routed to its input.
► True  (Page 117)  rep
► False

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Question No: 18  (Marks: 1) - Please choose one
Interrupt ______ is used to get or set the time.
► 0AH
► 1AH  (Page 136)
► 2AH
► 3AH

Question No: 19  (Marks: 1) - Please choose one
______ is used to set time.
► 1A/02H
► 1A/03H  (Page 138)
► 1A/04H
► 1A/05H

Question No: 20  (Marks: 1) - Please choose one
Communication between keyboard and keyboard controller is _________.
► Asynchronous serial
► Synchronous serial  (not sure)
► Parallel communication
► None of the given

MIDTERM EXAMINATION
Spring 2009
CS609- System Programming (Session - 1)

Question No: 1  (Marks: 1) - Please choose one
Following is not a method of I/O
► Programmed I/O
► Interrupt driven I/O
► Hardware Based I/O  (Page 4)
► None of given
Question No: 2 (Marks: 1) - Please choose one
The Function of I/O controller is to provide ___________.
► I/O control signals
► Buffering
► Error Correction and Detection
► All of given  (Page 5)  rep

Question No: 3 (Marks: 1) - Please choose one
Which of the following are types of ISR __________.
► BIOS (Basic I/O service) ISR
► DOS ISR
► ISR provided by third party device drivers
► All of the given  (Page 13)

Question No: 4 (Marks: 1) - Please choose one
Interrupt service number is usually placed in ____________ register.
► CH
► CL
► AH  (Page 26)  rep
► AL

Question No: 5 (Marks: 1) - Please choose one
NMI Stand for ____________
► Non Maskable Interrupt  (Page 46)
► Non Multitude Interrupt
► Non Maskable Instruction
► None of Given

Question No: 6 (Marks: 1) - Please choose one
A single interrupt controller can arbitrate among ____ different devices.
► 4
► 6
► 8  (Page 47)
► 10

Question No: 7 (Marks: 1) - Please choose one
Hardware Interrupts are ____________.
► Preemptive
► Non-Preemptive  (Page 48)  rep
► Both Preemptive and Non-Preemptive
► None of Given
Question No: 8  (Marks: 1) - Please choose one
The microprocessor package has many signals for data. Below are some in Correct priority order (Higher to Lower).

► Reset, Hold, NMI, INTR  (Page 46)
► NMI, INTR, Hold, Reset
► INTR, NMI, Reset, Hold
► None of the Given

Question No: 9  (Marks: 1) - Please choose one
The interval timer can operate in ____ modes.

► Three
► Four
► Five
► Six  (Page 72) rep

Question No: 10  (Marks: 1) - Please choose one
PPI stands for _____________.

► Parallel Programmable interface
► Peripheral Programmable interface  (Page 76) rep
► Port Programmable interface
► None of the given

Question No: 11  (Marks: 1) - Please choose one
The following command “outportb (0x61, inportb (0x61) & 0xFC),” will

► Turn on the speaker  (Page 74)
► Turn off the speaker
► Toggle the speaker
► None of the given

Question No: 12  (Marks: 1) - Please choose one
The PPI acts as an interface between the CPU and a parallel ________.

► I/O device  (Page 83) rep
► CPU
► BUS
► None of Given

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Question No: 13  (Marks: 1) - Please choose one
BIOS DO NOT support ______.
► LPT1
► LPT2
► LPT3
► LPT4  (Page 91)

Question No: 14  (Marks: 1) - Please choose one
____ bit is cleared to indicate the low nibble is being sent.
► D1
► D2
► D3
► D4  (Page 101)

Question No: 15  (Marks: 1) - Please choose one
The bit ______ of Line control register in UART, if cleared will indicate that DLL is the data register.
► 1
► 3
► 5
► 7  (Page 114)

Question No: 16  (Marks: 1) - Please choose one
Int 14H ________ can be used to set the line parameter of the UART or COM port.
► Service # 0  (Page 119)  rep
► Service # 1
► Service # 2
► Service # 3

Question No: 17  (Marks: 1) - Please choose one
_______ used to determine the amount of conventional memory interfaced with the processor in kilobytes.
► INT 10 H
► INT 11 H
► INT 12 H  (Page 162)
► INT 13 H
Question No: 18  (Marks: 1) - Please choose one
Bit number _______ of coprocessor control word is the Interrupt Enable Flag.

► 7  (Page 168)
► 8
► 9
► 10

Question No: 19  (Marks: 1) - Please choose one
To distinguish 486 with Pentium CPUID Test is used.

► True  (Page 166)
► False

Question No: 20  (Marks: 1) - Please choose one
______ is LED control byte.

► 0xF3
► 0xED  (Page 181) rep
► 0xE5
► 0xFF

MIDTERM EXAMINATION
Spring 2009
CS609- System Programming (Session - 1)

Question No: 1  (Marks: 1) - Please choose one
The memory addresses of COM ports remain same for all computers

► True
► False

Question No: 2  (Marks: 1) - Please choose one
Int__________ service 0 can be used to set the line parameter of the UART or COM port.

► 14H  (Page 119)
► 15H
► 13H
► None of the given option
Question No: 3  (Marks: 1) - Please choose one
In case of synchronous communication a timing signal is required to identify the start and end of a bit.

► True  (Page 105)  rep
► False

Question No: 4  (Marks: 1) - Please choose one
BIOS support ________ UARTS as COM ports.

► 6
► 4  (Page 113)
► 3
► 2

Question No: 5  (Marks: 1) - Please choose one
The ______ function uses the COM port number to receive a byte from the COM port using BIOS services.

► receivebyte()
► initialize ()
► receive()
► recievechar()  (Page 125)

Question No: 6  (Marks: 1) - Please choose one
DTE is _______

► Data terminal equipment  (Page 109)  rep
► Data transmitting equipment
► Dual terminal equipment
► None of above

Question No: 7  (Marks: 1) - Please choose one
Int 14H ______ can be used to send a byte

► Service#0
► Service#1  (Page 121)
► Service#2
► None of the given option.

Question No: 8  (Marks: 1) - Please choose one
Int 14H ______ can be used to set the line parameter of the UART or COM port

► Service#0  (Page 119)  rep
► Service#1
► Service#2
► None of the given option.
Question No: 9  (Marks: 1) - Please choose one
The ________ function initialize the COM port whose number is passed as parameter using BIOS services.

► Initializecom()
► Initialize()  (Page 125)
► Recievechar()
► None of these option

Question No: 10  (Marks: 1) - Please choose one
_____ whenever received indicates the start of communication and _____ whenever received indicates a temporary pause in the communication.

► XON & XOFF  (Page 135)
► XOFF & XON
► XON & YOFF
► YON & XOFF

Question No: 11  (Marks: 1) - Please choose one
_________ is used to identify cause of interrupt.

► Interrupt enable register
► Interrupt ID register  (Page 116) rep
► Interrupt Status register
► None of the above

Question No: 12  (Marks: 1) - Please choose one
In ___________ each byte is needed to be encapsulated in start and end.

► Synchronous communication
► Asynchronous communication  (Page 106)
► Both
► None of given

Question No: 13  (Marks: 1) - Please choose one
_________ is a device incorporated into the PC to update time even if the computer is off.

► Clock counter
► ROM
► Clock
► Real time clock  (Page 136)
Question No: 14  (Marks: 1) - Please choose one
There are two types of communication synchronous and Anti Synchronous
▶ True
▶ False (Page 105)

Question No: 15  (Marks: 1) - Please choose one
The ____________ function simply enables the self test facility within the modem control register
▶ STOn()
▶ SelfTest()
▶ SelfTestOn() (Page 127)
▶ Non of these

Question No: 16  (Marks: 1) - Please choose one
DCE is ___________
▶ Data communication equipment (Page 109) rep
▶ Dual communication equipment
▶ Data clearing equipment
▶ None of given

Question No: 17  (Marks: 1) - Please choose one
REGS is a Union
▶ True
▶ False

Question No: 18  (Marks: 1) - Please choose one
In self test mode the output of the UART is routed to its input
▶ True (Page 117) rep
▶ False

تم اجابة كل قسمة من السؤالاس يمكن تكرارها بكم مرة واردة بالنقطة ثم كواحد ضعيف
Question No. 1 Marks : 2
If CPUID instruction is not present then the processor can be a
► 486 processor  (Page 166)
► 386 processor
► 286 processor
► All of the above

Question No. 2 Marks : 2
On the execution of IRET instruction, number of bytes popped from stack is
► 4 bytes
► 6 bytes
► 8 bytes  Click here for detail
► 10 bytes

Question No. 3 Marks : 2
Keyboard Status Byte is located at the address
► 0040:0000H
► 0040:0013H
► 0040:0015H
► 0040:0017H  (Page 29)

Question No. 4 Marks : 2
If we use keep (0, 1000) in a TSR program, the memory allocated to it is
► 64000 bytes
► 32000 bytes
► 16000 bytes
► 80000 bytes

Question No. 5 Marks : 2
Maximum number of interrupts in a standard PC is
► 64
► 128
► 256  (Page 10)
► 512
CS609 – Solved Quizzes (1 & 2)

Quiz No.1

Question : 1 of 10  ( Marks: 1 ) - Please choose one
Total No. of bytes that can be stored in Keyboard Buffer is____.

►16
►32 (Page 54)
►64
►128

Question : 2 of 10  ( Marks: 1 ) - Please choose one
If printer is _____ then printer sends back the ACK signal to the printer interface

►idle (Page 97)
►busy
►Out of paper
►None of the given

Question : 3 of 10  ( Marks: 1 ) - Please choose one
The baud rate is set in accordance with the divisor value loaded within the UART internal registers base +0 and base +1.

►TRUE (Page 114)
►FALSE

Question : 4 of 10  ( Marks: 1 ) - Please choose one
Int 14H __________ can be used to receive a byte.

►Service # 0
►Service # 1
►Service # 2 (Page 121)
►None of the given options

Question : 5 of 10  ( Marks: 1 ) - Please choose one
The BIOS interrupt 0x1AH can be used to configure real time clock

►True (Page 136)
►False
Question : 6 of 10  ( Marks: 1 ) - Please choose one
----------- is used to read date from RTC
► 1A\02H
► 1A\03H
► 1A\04H  (Page 138)
► 1A\05H

Question : 7 of 10  ( Marks: 1 ) - Please choose one
The -------- function receive a byte and COM port number is passed as parameter using BIOS service
► Receivebyte ();
► Receive ();
► Recievechar ();  (Page 125)
► None of the given option

Question : 8 of 10  ( Marks: 1 ) - Please choose one
----------- whenever receive indicates the start of communication .......... whenever receive indicates the end of communication
► XON\XOFF  (Page 135)
► XOFF\XON
► XON\YOFF
► YON\XOFF

Question : 9 of 10  ( Marks: 1 ) - Please choose one
----------- is used to read time from RTC
► 1A\02H  (Page 137)
► 1A\03H
► 1A\04H
► 1A\05H

Question : 10 of 10  ( Marks: 1 ) - Please choose one
----------- is used to set time from RTC
► 1A\02H
► 1A\03H  (Page 138)
► 1A\04H
► 1A\05H
Quiz No.2

Question : 1 of 10  ( Marks: 1 ) - Please choose one
Software based flow control make use of -------- control characters
► Xon
► XOFF
► Both  (Page 135)
► None

Question : 2 of 10  ( Marks: 1 ) - Please choose one
What will be the value of DL register when we are accessing C drive using undocumented service 21H/32H?
► 0
► 1
► 2
► 3  (Page 249)

Question : 3 of 10  ( Marks: 1 ) - Please choose one
Set the Interrupt vector means to change the double word sized interrupt vector within IVT.
► True  (Page 22)
► False

Question : 4 of 10  ( Marks: 1 ) - Please choose one
If keyboard buffer is empty the head and tail points at the same location.
► True  (Page 55)
► False

Question : 5 of 10  ( Marks: 1 ) - Please choose one
Standard PC can have _____ PPI.
► 1
► 4  (Page 84)
► 8
► 16

Question : 6 of 10  ( Marks: 1 ) - Please choose one
By cascading two DMAs _____ bits can be transferred.
► 4
► 8
► 16  (Page 186)
► 32
Question : 7 of 10  (Marks: 1) - Please choose one
PPI interconnection _______ bits is cleared to indicate low nibble is being sent.

►D1
►D2
►D3
►D4  (Page 101)

Question : 8 of 10  (Marks: 1) - Please choose one
File can be______ viewed as collection of clusters or blocks.

►Physically  (Page 256)
►Logically
►Both physically and logically
►None

Question : 9 of 10  (Marks: 1) - Please choose one
Operating system name contains ____ bytes in boot block.

►3
►5
►8  (Page 257)
►11

Question : 10 of 10  (Marks: 1) - Please choose one
File system used in CD’s is_______ file system

►Contiguous   http://www.pgallert.de/english/SysAdmin/OS/file.htm
►Chained
►Indexed
►None

Question : 1 of 10  (Marks: 1) - Please choose one
________means typematic rate will be sent in next byte.

►0xF3  (Page 180)
►0xF4
►0xF5
►0xF6
Question : 2 of 10  (Marks: 1) - Please choose one
To access the block within cluster using BIOS services the cluster number should be converted into _____.

► CHS
► LBA
► LSN  (Page 258)
► None of the given

Question : 3 of 10  (Marks: 1) - Please choose one
Display device (Monitor) performs _______ I/O.

► memory mapped  (Page 30)
► Isolated
► Both of above
► None of these

Question : 4 of 10  (Marks: 1) - Please choose one
Timer interrupt occurs ______ times every second by means of hardware.

► 18.2  (Page 28)
► 16.2
► 15.2
► 14.2

Question : 5 of 10  (Marks: 1) - Please choose one
An I/O device cannot be directly connected to the busses so controller is placed between CPU and I/O.

► True  (Page 83)
► False

Question : 6 of 10  (Marks: 1) - Please choose one
Keyboard uses port ____ as status port.

► 64H  (Page 177)
► 66H
► 67H
► 69H

Question : 7 of 10  (Marks: 1) - Please choose one
Tail of keyboard should get to get the start of buffer.

► True  (Page 55)
► False
Question : 4 of 10  (Marks: 1) - Please choose one
Control information in files is maintained using

► BPB
► DPB
► FCB  (Page 256)
► FPB

Question : 5 of 10  (Marks: 1) - Please choose one
____ No. of bytes are used to store the character in the keyboard buffer.

► 1
► 2  (Page 54)
► 4
► 8

Question : 6 of 10  (Marks: 1) - Please choose one
The ______ service # is not used in any interrupt.

► 01
► 02
► 03
► FF

Question : 7 of 10  (Marks: 1) - Please choose one
At IRQ 7 Interrupt # ____ is used.

► 0x0A
► 0x0B
► 0x0C
► 0x0F  (Page 95)

Question : 8 of 10  (Marks: 1) - Please choose one
We have set the bit No. 7 of IMR(Interrupt Mask Register) to unmask the Interrupt so that interrupt _____ can occur at ____ line.

► 0xf , IRQ 7
► 0xa, IRQ 6
► 0x8, IRQ 5
► 0x6, IRQ 2
Question : 9 of 10  (Marks: 1) - Please choose one
If we want to produce the grave voice from speaker phone then we have to load the ___ divisor values at Port ___.
► high, 0x42
► low, 0x22
► high, 0x22
► low, 0x42

Question : 10 of 10  (Marks: 1) - Please choose one
In order to produce the sound from PC internal Speaker we have to load the___ bit divisor value at the ___ port.
► 8, 0x21
► 16, 0x42
► 32, 0x22
► 64, 0x32

Question : 1 of 10  (Marks: 1) - Please choose one
In counter register bit no. 3 changes its value between 0 and 1 with in ____ clock cycles
► 1
► 2
► 4
► 16 (Page 69)

Question : 2 of 10  (Marks: 1) - Please choose one
In keyboard status byte bit no. 2 and 3 are used for ctrl and alt keys respectively. which of the following condition is used to check that Ctrl + Alt keys are pressed. Where: unsigned char far * scr = (unsigned char far *)(0x00400017);
► if (((*scr)&12)==12)
► if (((*scr)&8)==8)
► if (((*scr)&4)==4)
► if (((*scr)&2)==2)