CS401 Assembly language

Grand Quiz Solved 100 % Confirm (Covid-19)

By Sadiq Ahmed & Sonu Ilyas Mughal

Contact VU-Tech Soft

A Complete Academy For all subjects

Contact if you want complete set of Solved Notes

03063548328 WhatsApp

https://www.youtube.com/playlist?list=PLtCZKi9LoftVmx-c_Y1_PsBPAyF83saG

For Cs401 Students

Step by Step Topics of Computer Architecture &Assembly Language programming

Watch these videos

More Solved Quiz Will be share today inshaALLAH
Question # 1 of 30 (Start time: 12:15:28 AM, 19 July 2020)

 can also be used as a masking operation to invert selective bits.

Select the correct option

- OR
- NOT
- AND
- XOR

Click to Save Answer & Move to Next Question
Question 2 of 30 (Start time: 12:15:55 AM, 19 July 2020)

DX plays an important role in arithmetic ________

Select the correct option:
- addition
- multiplication
- division
- subtraction

Click to Save Answer & Move to Next Q
CS401: Grand Quiz

Question #3 of 30 (Start time: 12:17:26 AM, 19 July 2020)

Stack is:

Select the correct option:

- an algorithm
- a data structure
- both algorithm and data structure
- a procedure

03063548328
Question # 4 of 30 ( Start time: 12:18:05 AM, 19 July 2020 )

REPE or REPE are used with the _____ instructions.

Select the correct option

- SCAS
- MOV
- ADD
- CALL
Question # 5 of 30 (Start time: 12:18:49 AM, 19 July 2020)

Instructions have two parameters, one is the general purpose register to be loaded and the other is the memory location from which to load these registers.

Select the correct option:

- SCAS
- REPNE
- CALL
- LDS
**Question # 6 of 30 ( Start time: 12:18:41 AM, 19 July 2020 )**

Keyword used to define two bytes in a program?

<table>
<thead>
<tr>
<th>Select the correct option</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Dw</strong></td>
</tr>
<tr>
<td><strong>Db</strong></td>
</tr>
<tr>
<td><strong>Dn</strong></td>
</tr>
<tr>
<td><strong>Bw</strong></td>
</tr>
</tbody>
</table>
Question # 7 of 30 ( Start time: 12:19:56 AM, 19 July 2020 )

containing the address of the next instruction to be executed

Select the correct option

<table>
<thead>
<tr>
<th>Option</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Instruction Pointer (IP)</td>
<td></td>
</tr>
<tr>
<td>Stack Pointer (SP)</td>
<td></td>
</tr>
<tr>
<td>Base Pointer (BP)</td>
<td></td>
</tr>
<tr>
<td>Flags Register</td>
<td></td>
</tr>
</tbody>
</table>
Question # 8 of 30 (Start time: 12:20:48 AM, 19 July 2020)
The shift logical left operation is the exact _______ of shift logical right.

<table>
<thead>
<tr>
<th>Select the correct option</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Similar</td>
<td></td>
</tr>
<tr>
<td>Opposite</td>
<td></td>
</tr>
<tr>
<td>Implementation</td>
<td></td>
</tr>
<tr>
<td>comparison</td>
<td></td>
</tr>
</tbody>
</table>
CS401: Grand Quiz

Question # 11 of 30  (Start time: 12:23:30 AM, 19 July 2020)

Sending the appropriate signals on the control bus to the memory is the responsibility of _________

Select the correct option
- Memory
- Processor
- Control Bus
- Data Bus

03063548328
Question # 12 of 30 (Start time: 12:24:03 AM, 19 July 2020)

A parallel port has _______ views.

Select the correct option

- [ ] 2
- [ ] 4
- [ ] 8
- [ ] 1
Question # 13 of 30  ( Start time: 12:24:33 AM, 19 July 2020 )

The mechanism used to drop carry for making the calculated address valid is known as:

Select the correct option

- Carry Overflow
- Overflow
- Address Wraparound
- Segment Overlapping
Question # 14 of 30 (Start time: 12:25:15 AM, 19 July 2020)

In ___________, a zero is inserted from the right and every bit moves one position to its left with the most significant bit dropping into the carry flag.

Select the correct option

- [ ] SHL
- [ ] SAL
- [ ] SAR
- [x] Both SHL and SAL
**Question # 16 of 30 ( Start time: 12:26:38 AM, 19 July 2020 )**

The reduction in code size and the improvement in speed are the two reasons why block processing instructions were introduced in the _________ processor.

Select the correct option

<table>
<thead>
<tr>
<th>Option</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>8088</td>
<td></td>
</tr>
<tr>
<td>8085</td>
<td></td>
</tr>
<tr>
<td>8080</td>
<td></td>
</tr>
<tr>
<td>IAPX388</td>
<td></td>
</tr>
</tbody>
</table>
The base pointer accesses local variables using \_\_\_\_\_\_\_\_\_ offsets.

Select the correct option:

- positive
- incremental
- fixed
- negative
CS401: Grand Quiz

Question # 18 of 30 (Start time: 12:28:52 AM, 19 July 2020)

MOV AX, [NUM1] is a ______ bit move Instruction.

Select the correct option:

- 8
- 16
- 24
- 32

Question # 19 of 30 (Start time: 12:29:05 AM, 19 July 2020)

In case of 32-bit processor, the size of an accumulator register will be ______ bits.

Select the correct option:

- 16
- 8
- 32
- 64
Question # 20 of 30 ( Start time: 12:29:32 AM, 19 July 2020 )
Which of the following is the interrupt number for debug interrupt?

Select the correct option

- INT 0
- INT 1
- INT 2
- INT 3

Question # 21 of 30 ( Start time: 12:29:53 AM, 19 July 2020 )
Each entry of the interrupt vector table is of _________ bytes.

Select the correct option

- 1
- 2
- 3
- 4
Question # 22 of 30 ( Start time: 12:30:28 AM, 19 July 2020 )

If BL contains 00000101 then after a Single Right Shift, BL will contain

Select the correct option:

- 00000111
- 0000010
- 10000111
- 10000010
- 10000010
Question # 24 of 30 (Start time: 12:32:13 AM, 19 July 2020)

BH register is a ____ bit register.

Select the correct option

8
16
24
32
Question # 25 of 30 (Start time: 12:32:03 AM, 19 July 2020)

can be used to check whether particular bits of a number are set or not.

Select the correct option

- OR
- AND
- NOT
- XOR

Question # 26 of 30 (Start time: 12:33:22 AM, 19 July 2020)

The Jump command that does not depend on FLAG register is

Select the correct option

- JNZ
- JD
- JNE
- JP
Question # 27 of 30  ( Start time: 12:33:35 AM, 19 July 2020 )

When the relative address stored with the instruction is in 16-bits, the jump is called a _______ jump.

Select the correct option

- Near
- Short
- Far
- Long
The stack of 8088 works on _______ sized elements.

Select the correct option:
- word
- byte
- nibble
- paragraph
Question # 29 of 30  
(Start time: 12:35:15 AM, 19 July 2020)  
The interrupt call loads new values in _______ segment.

Select the correct option

- ES
- FLAG
- DX
- SS
### Question # 30 of 30 (Start time: 12:36:11 AM, 19 July 2020)

Twenty-bit register is formed by the combination of two ______ bit registers.

<table>
<thead>
<tr>
<th>Option</th>
<th>Number of bits</th>
</tr>
</thead>
<tbody>
<tr>
<td>Eight</td>
<td>10</td>
</tr>
<tr>
<td>Four</td>
<td>4</td>
</tr>
<tr>
<td>Sixteen</td>
<td>16</td>
</tr>
<tr>
<td>Twelve</td>
<td>12</td>
</tr>
</tbody>
</table>
Question # 1 of 30 (Start time: 01:43:00 AM, 19 July 2020)

```assembly
MOV AX, 0XB800
MOV ES, AX; This instruction points ES to______.
```

Select the correct option

<p>| | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>video base</strong></td>
<td></td>
</tr>
<tr>
<td><strong>audio base</strong></td>
<td></td>
</tr>
<tr>
<td><strong>graphics base</strong></td>
<td></td>
</tr>
<tr>
<td><strong>numeric base</strong></td>
<td></td>
</tr>
</tbody>
</table>

CONTACT FOR SOLVING QUIZ

03063548328
Question # 2 of 30 (Start time: 01:44:00 AM, 19 July 2020)

When the operand of DIV instruction is of 16 bits then implied dividend will be of ____ bits.

Select the correct option

16

32

64

8
Question # 3 of 30 (Start time: 01:44:45 AM, 19 July 2020)

Which bit of attribute byte represents the blue component of foreground color?

Select the correct option:

0

1

2

3
Question # 25 of 30 (Start time: 02:00:54 AM, 19 July 2020)

When the stack pointer points to the return address?

Select the correct option

- When the bubble sort subroutine is called
- When the subroutine is called (Correct)
- When the instruction is called
- When the arguments is called
Question # 26 of 30 (Start time: 02:02:15 AM, 19 July 2020)

When the operand of DIV instruction is of 16-bits then implied dividend will be stored in _____.

Select the correct option

1. AX register

2. The concatenation of DX and AX

3. The concatenation of ES and AX

4. The concatenation of DS and BX
Question # 27 of 30 ( Start time: 02:03:43 AM, 19 July 2020 )

Constant can never be used as _________.

Select the correct option

- source
- destination
- both source and destination
- immediate source
Question # 28 of 30 (Start time: 02:04:41 AM, 19 July 2020)

DB-25 is a _______ port connector.

Select the correct option

- partially serial
- half duplex
- serial
- parallel

CONTACT FOR SOLVING QUIZ

03063548328
Question # 29 of 30 ( Start time: 02:05:12 AM, 19 July 2020 )

Flags register is a special register in every architecture, it is also known as ____________

Select the correct option

- Carry Flag
- Register
- Program Status Word
- Special Register
Question # 30 of 30 ( Start time: 02:06:06 AM, 19 July 2020 )

To multiply a number in a register by 2 the number is ____________.

Select the correct option

- Shifted right one bit
- Rotated right one bit
- Shifted left one bit
- Rotated left one bit
Question # 1 of 30 (Start time: 02:18:42 AM, 19 July 2020)

In Far jump ____________.

Select the correct option

- Only offset is given
- Only segment address is given
- Both offset and segment are given
- No need to mention any segment or offset
Question # 2 of 30 (Start time: 02:20:06 AM, 19 July 2020)

Which of the following instruction is effectively same as to multiply the value of AX by 8?

Select the correct option

- [ ] SHL AX, 8
- [ ] SHL AX, 3
- [x] MUL AX, 3
- [ ] SAL AX, 8
Question # 4 of 30  (Start time: 02:22:49 AM, 19 July 2020)

_________ and _________ are taken if the last arithmetic operation produced a number in its destination that has odd parity.

Select the correct option

- JP, JPE
- JS, JNS
- JCX, JNS
- JNP, JPO
In case of a downward compatible mechanism, the codes written for 8088 are ________ for 386 processor.

Select the correct option

- Control
- Valid
- Invalid
- Alter

CONTACT FOR SOLVING QUIZ
03063548328
Question # 6 of 30 (Start time: 02:24:36 AM, 19 July 2020)

is a mechanism that is used for mapping interrupts to interrupt handlers.

Select the correct option

<table>
<thead>
<tr>
<th>Option</th>
</tr>
</thead>
<tbody>
<tr>
<td>Interrupt Invoking</td>
</tr>
</tbody>
</table>
| Interrupt Hooking   | **Correct**
| Interrupt Matching  |
| Interrupt Calling   |
Question # 7 of 30 (Start time: 02:25:53 AM, 19 July 2020)

In the instruction "mov word [es:160], 0x1230", 12 means:

Select the correct option

- green color on black background
- green color on blue background
- black color on green background
- blue color on green background
Question # 8 of 30 (Start time: 02:26:45 AM, 19 July 2020)

Which of the following statement is used to clear the value of AX register?

Select the correct option

- XOR AX, AX
- AND AX, AX
- XOR AX, 0
- NOT AX
Question # 9 of 30 (Start time: 02:28:00 AM, 19 July 2020)
Which assembly instruction is used to ADD data at address 1200 to bx register?

Select the correct option

- add bx, [1200]
- add bx, 1200
- add ax, [1200]
- add bx, [bx + 1200]
Question # 10 of 30  (Start time: 02:29:08 AM, 19 July 2020)

Twenty-bit register is formed by the combination of two _______ bit registers.

Select the correct option

- Four
- Sixteen
- Twelve
Choose the correct option from the following addressing modes, from which both registers moves into the Data Segment?

Select the correct option

- Base + Index
- Base + Offset
- Index + Offset
- Base + Index + Offset
Question # 12 of 30  (Start time: 02:31:02 AM, 19 July 2020)

90 is the op-code of

Select the correct option

- Do nothing
- Add
- Subtract
- Multiplication
Question # 13 of 30 (Start time: 02:31:12 AM, 19 July 2020)

In ______ operation, a carry flag is inserted from the left moving every bit one position to the right, with the right most bit is dropped in the carry flag.

Select the correct option

<p>| | | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>RCR</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ROL</td>
<td></td>
<td></td>
</tr>
<tr>
<td>RCL</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ROR</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
Question # 14 of 30  ( Start time: 02:31:59 AM, 19 July 2020 )
What does the given instruction do?
SHR DL, 1

Select the correct option

- move right most bit in carry
- move bit in DL
- Shift and store the One bit in DL
- Shift and store the most bit in DL
Question # 15 of 30 (Start time: 02:33:19 AM, 19 July 2020)

After the execution of SAR instruction, __________.

Select the correct option

- The msb is replaced by a 0
- The msb retains its original value
- The msb is replaced by 1
- The msb is replaced by the value of CF
Question # 16 of 30 (Start time: 02:34:05 AM, 19 July 2020)

Which of the following instructions is used for non-destructive AND operation?

Select the correct option

- CMP
- NAND
- TEST
- ADC
**Question # 17 of 30** *(Start time: 02:34:38 AM, 19 July 2020)*

Which of the following is the most illegal instruction?

<table>
<thead>
<tr>
<th>Select the correct option</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Mov al, [num1]</td>
<td></td>
</tr>
<tr>
<td>Mov al, ax</td>
<td>✓</td>
</tr>
<tr>
<td>Mov ax, [num1]</td>
<td></td>
</tr>
<tr>
<td>Mov ax, al</td>
<td></td>
</tr>
</tbody>
</table>

**CONTACT FOR SOLVING QUIZ**

03063548328
Question # 18 of 30 ( Start time: 02:34:55 AM, 19 July 2020 )

___________ is part of microprocessor that manages the execution of instructions.

Select the correct option

- Arithmetic/Logic Unit
- Registers
- Control Unit
- Memory
CS401: Grand Quiz

Question # 19 of 30 (Start time: 02:35:45 AM, 19 July 2020)

A computer instruction can be used to check whether particular bits of a number are set or not.

Select the correct option

- OR
- AND
- NOT
- XOR
Question # 20 of 30 (Start time: 02:36:07 AM, 19 July 2020)

IAAPX88 architecture consists of

Select the correct option

1. 10 Registers
2. 14 Registers (√)
3. 16 Registers
4. 32 Registers


**Question:** What does ST1 stand for?

**Options:**
- Set interrupts
- Set the interrupt flag
- Set instruction
- Standard time instruction
Question # 22 of 30 (Start time: 02:37:13 AM, 19 July 2020)
Stack is a data structure that behaves in a first in last ______ manner.

Select the correct option

- In
- Out
- Push
- Add
Question # 23 of 30 (Start time: 02:37:57 AM, 19 July 2020)

In a comparison, if the both operands are same. The result of subtraction will be zero and the zero flag will be _______.

Select the correct option

1. Set
2. Incremented
3. Updated
4. Decremented
Question # 24 of 30 ( Start time: 02:38:14 AM, 19 July 2020 )

When SI or DI are used, we name the method.

<table>
<thead>
<tr>
<th>Select the correct option</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Based Addressing</td>
<td></td>
</tr>
<tr>
<td>Indexed Addressing</td>
<td>✔</td>
</tr>
<tr>
<td>Stack Addressing</td>
<td></td>
</tr>
<tr>
<td>Segment addressing</td>
<td></td>
</tr>
</tbody>
</table>
Question # 25 of 30  (Start time: 02:38:24 AM, 19 July 2020)

Flags register is a special register in every architecture, it is also known as ____________

Select the correct option

- Carry Flag
- Register
- Program Status Word
- Special Register
CS401: Grand Quiz

Question # 26 of 30 (Start time: 02:38:39 AM, 19 July 2020)

Which combination will you prefer to obtain the physical address of the stack?

Select the correct option

<table>
<thead>
<tr>
<th>Option</th>
<th>Label</th>
</tr>
</thead>
<tbody>
<tr>
<td>SS:SI</td>
<td></td>
</tr>
<tr>
<td>SS:SP</td>
<td></td>
</tr>
<tr>
<td>SP:DI</td>
<td></td>
</tr>
<tr>
<td>SP:SI</td>
<td></td>
</tr>
</tbody>
</table>
Question # 27 of 30 ( Start time: 02:39:31 AM, 19 July 2020 )

Whenever we need access to a memory location whose address is not known until run-time we use ________.

Select the correct option

- Destination Register
- Counter Register
- Accumulator Register
- Index Register

CONTACT FOR SOLVING QUIZ
03063548328
There are ________ registers in iAPX88 architecture that can hold address of data.

Select the correct option

1. 
2. 
3. 
4. ✗
Question # 29 of 30  ( Start time: 02:40:49 AM, 19 July 2020 )

ES instruction loads register

Select the correct option

- ES
- DS
- AX
- DX
Question # 30 of 30  (Start time: 02:41:14 AM, 19 July 2020)

Interrupts are _________ events.

Select the correct option

- Asynchronous
- Synchronous
- Predictable
- Dependent
Question # 1 of 30 ( Start time: 02:57:21 AM, 19 July 2020 )

Keyword used to define two bytes in a program?

Select the correct option:

- Dw
- Db
- Dn
- Bw
Question # 3 of 30 (Start time: 02:59:05 AM, 19 July 2020)

During program execution, if any change in AH or AL is reflected in __________ as well.

Select the correct option

- AL
- AX
- AH
- BX
Question # 4 of 30 (Start time: 02:59:15 AM, 19 July 2020)

Interrupts are ________ events.

Select the correct option

- Asynchronous
- Synchronous
- Predictable
- Dependent
Question # 5 of 30 (Start time: 02:59:25 AM, 19 July 2020)

___ can be used to check whether particular bits of a number are set or not.

Select the correct option

- OR
- AND
- NOT
- XOR
mov [1234], ax is an example of

Select the correct option

- direct addressing
- base register indirect
- base+index addressing
- register indirect addressing
The interrupt call loads new values in ______ segment.

Select the correct option

- ES
- FLAG
- BX
- SS
Question # 8 of 30 ( Start time: 03:00:28 AM, 19 July 2020 )

In __________, a zero is inserted from the right and every bit moves one position to its left with the most significant bit dropping into the carry flag.

<table>
<thead>
<tr>
<th>Option</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>SHL</td>
<td></td>
</tr>
<tr>
<td>SAL</td>
<td></td>
</tr>
<tr>
<td>SAR</td>
<td></td>
</tr>
<tr>
<td>Both SHL and SAL</td>
<td></td>
</tr>
</tbody>
</table>
Question # 9 of 30 ( Start time: 03:00:50 AM, 19 July 2020 )

When the RET instruction is executed, it recovers the value of the ___________ from the stack.

Select the correct option

- Current instruction
- Previous instruction
- Accumulator register
- Instruction pointer
Question # 10 of 30 (Start time: 03:01:26 AM, 19 July 2020)

The basic function of register is to

Select the correct option

- Hold the operand
- Hold the operator
- Hold both the operator and operand
- None of the given
Question # 11 of 30  

Start time: 03:02:36 AM, 19 July 2020

In For jump __________.

Select the correct option

- Only offset is given
- Only segment address is given
- Both offset and segment are given
- No need to mention any segment or offset
Question # 13 of 30 (Start time: 03:04:27 AM, 19 July 2020)
The stack pointer contains the address of the word that is currently on _____:

Select the correct option

- Top of the stack
- Down of the stack
- Top and Down both
- Any position in the stack
Question # 14 of 30 ( Start time: 03:04:55 AM, 19 July 2020 )

In the operation of __________ instruction, the most significant bit is copied to the carry flag and inserted from the right, causing every bit to move one position to the left.

Select the correct option

- Rotate Left (ROL)
- Rotate Through Carry Right (RCR)
- Rotate Through Carry Left (RCL)
- Rotate Right (ROR)
Question # 15 of 30  (Start time: 03:06:21 AM, 19 July 2020)

SCAS compares a source byte or word in register AL or AX with the ____________ string element addressed by ES: DI and updates the flags.

<table>
<thead>
<tr>
<th>Select the correct option</th>
</tr>
</thead>
<tbody>
<tr>
<td>Source</td>
</tr>
<tr>
<td>Destination</td>
</tr>
<tr>
<td>Flag</td>
</tr>
<tr>
<td>Register</td>
</tr>
</tbody>
</table>
Question # 16 of 30  ( Start time: 03:07:05 AM, 19 July 2020 )

In _____ operation, a carry flag is inserted from the left moving every bit one position to the right, with the right most bit is dropped in the carry flag".

Select the correct option

- RCR
- ROL
- RCL
- ROR
Question # 17 of 30 ( Start time: 03:08:14 AM, 19 July 2020 )
_________ is set only when the last mathematical or logical operation produces a zero in its destination.

Select the correct option

- Sign Flag
- Zero flag
- Trap Flag
- Parity Flag
Question # 18 of 30 (Start time: 03:08:40 AM, 19 July 2020)

The keyword used for the conditional jumps is ______.

Select the correct option

- NEAR
- FOR
- SHORT
- OPTIONAL
**Question # 19 of 30** (Start time: 03:09:33 AM, 19 July 2020)

_________ decrements SP (the stack pointer) by two and then transfers a word from source operand to the top of stack now pointed to by SP.

**Select the correct option**

<table>
<thead>
<tr>
<th>Option</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>☑️</td>
<td>PUSH</td>
</tr>
<tr>
<td></td>
<td>POP</td>
</tr>
<tr>
<td></td>
<td>CALL</td>
</tr>
<tr>
<td></td>
<td>RET</td>
</tr>
</tbody>
</table>
Question # 20 of 30 (Start time: 03:10:03 AM, 19 July 2020)

STI stands for?

Select the correct option

- Set interrupts
  - [ ]
- Set the interrupt flag
  - [ ]
- Set instruction
  - [ ]
- Standard time instruction
  - [ ]
Question # 21 of 30 (Start time: 03:10:24 AM, 19 July 2020)

SP is associated (by default) with ________.

Select the correct option

- DS
- SS
- ES
- CS
CS401: Grand Quiz

Question # 22 of 30 (Start time: 03:11:10 AM, 19 July 2020)

__________ is the basic means of storing temporary data on the stack.

Select the correct option

- PUSH
- POP
- CALL
- RET
Question # 23 of 30 (Start time: 03:11:52 AM, 19 July 2020)

In string manipulation, the instruction to clear the direction flag is:

Select the correct option

- cl df
- clrd
- clr df
- clrd

CONTACT FOR SOLVING QUIZ
03063548328
**Question # 24 of 30 (Start time: 03:12:27 AM, 19 July 2020)**

8088 is a ________ processor with its accumulator and all registers of ________.

<table>
<thead>
<tr>
<th>Select the correct option</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>16 bit, 32 bits</td>
<td></td>
</tr>
</tbody>
</table>
| 16 bit, 16 bits           |   | **(Correct Answer)**
| 8 bit, 8 bits             |   |
| 32 bit, 32 bits           |   |
Question # 25 of 30  (Start time: 03:13:09 AM, 19 July 2020)

"mov [bx], ax" moves the two bytes contents of the AX register to the address contained in BX register in the current ________.

Select the correct option

- Stack Segment
- Data Segment
- Code Segment
- Extra Segment
Question # 26 of 30  (Start time: 03:14:10 AM, 19 July 2020)

Which combination will you prefer to obtain the physical address of the stack?

Select the correct option

- SS:SI
- SS:SP
- SP:DI
- SP:SI
Question # 27 of 30  (Start time: 03:14:38 AM, 19 July 2020)
Which among the following is the pointer registers?

Select the correct option

- Base pointer and stack pointer
- Index pointer and decision pointer
- Service pointer and decision pointer
- Stack pointer and index pointer
Question # 28 of 30 ( Start time: 03:15:35 AM, 19 July 2020 )

Which of the following is also called intra-segment call?

Select the correct option

- Long call
- Near call
- Far call
- Short call
Question # 29 of 30 (Start time: 03:16:57 AM, 19 July 2020)

Which of the following shift operation inserts a zero from the left and moves every bit one position to the right and copies the rightmost bit in the carry flag?

Select the correct option

- SHL
- SAL
- SAR
- SHR

03063548328
Question # 30 of 30 (Start time: 03:18:00 AM, 19 July 2020)

“Far” jump is not position relative but is ________.

Select the correct option

- Memory Dependent
- Absolute
- Temporary
- Indirect
Question # 1 of 30 (Start time: 03:22:20 AM, 19 July 2020)

Twenty-bit register is formed by the combination of two ______ bit registers.

Select the correct option

- Eight
- Four
- Sixteen
- Twelve

03063548328
Question # 2 of 30 (Start time: 03:22:40 AM, 19 July 2020)

If 'A' is subtracted from 'B' and the resulting answer is negative figures it means B is?

Select the correct option

- Large number
- Small number (Correct)
- Positive number
- Equal number
Question # 3 of 30 ( Start time: 03:23:13 AM, 19 July 2020 )

Sending the appropriate signals on the control bus to the memory is the responsibility of _____________.

Select the correct option

- Memory
- Processor
- Control Bus
- Data Bus
Question # 4 of 30 (Start time: 03:23:48 AM, 19 July 2020)

_________ interrupts are those which occur side by side with some other activity.

Select the correct option

- Asynchronous
- Synchronous
- Predictable
- Maskable
Question # 5 of 30 ( Start time: 03:24:22 AM, 19 July 2020 )

Jump is not position relative but is absolute

Select the correct option

<table>
<thead>
<tr>
<th></th>
<th>Near</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Short</td>
</tr>
<tr>
<td>○</td>
<td>Far</td>
</tr>
<tr>
<td></td>
<td>Extra</td>
</tr>
</tbody>
</table>
Question # 6 of 30 ( Start time: 03:24:55 AM, 19 July 2020 )

The correlation process from the interrupt number to the interrupt handler uses a table called ________.

Select the correct option

- [ ] Interrupt Calling Table
- [ ] Interrupt Handling Table
- [x] Interrupt Vector Table
- [ ] Interrupt Correlation Table
CS401: Grand Quiz

Question # 7 of 30 (Start time: 03:25:16 AM, 19 July 2020)

In ________ operation, the output is 1 only if both inputs are 1.

Select the correct option

- AND
- OR
- NOT
- XOR
Question # 8 of 30 (Start time: 03:25:36 AM, 19 July 2020)

When a 32 bit number is divided by a 16 bit number, the remainder is of

Select the correct option

- 4 bits
- 8 bits
- 16 bits
- 32 bits
Question # 10 of 30 (Start time: 03:27:02 AM, 19 July 2020)

_________ decrements SP (the stack pointer) by two and then transfers a word from source operand to the top of stack now pointed to by SP.

Select the correct option

- PUSH
- POP
- CALL
- RET
Question # 11 of 30 (Start time: 03:27:12 AM, 19 July 2020)

There are just _____ block processing instructions in 8088.

Select the correct option

- 6
- 5 (Correct)
- 4
- 3
Question # 12 of 30  (Start time: 03:27:23 AM, 19 July 2020)

SS is by default associated with

Select the correct option

- BP
- IP
- SP
- BP
Which of the following is also called intra-segment call?

Select the correct option

- Long call
- Near call (Correct)
- Far call
- Short call
Question # 14 of 30 ( Start time: 03:27:46 AM, 19 July 2020 )

also known as source operand since the data is moving to the stack from this operand.

Select the correct option

- PUSH
- POP
- CALL
- RET
Question # 15 of 30 ( Start time: 03:28:12 AM, 19 July 2020 )

In general, width of a memory cell cannot be greater than the width of __________.

Select the correct option

- Control Bus
- Data Bus
- Address Bus
- I/O Bus
Question # 16 of 30 (Start time: 03:28:41 AM, 19 July 2020)

Which combination will you prefer to obtain the physical address of the stack?

Select the correct option

- SS:SI
- SS:SP
- SP:DI
- SP:SI
**Question # 17 of 30**  
**Start time: 03:28:51 AM, 19 July 2020**

What does the given instruction do?  
SHR DL, 1

**Select the correct option**

<table>
<thead>
<tr>
<th>Option</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>☑️</td>
<td>move right most bit in carry</td>
</tr>
<tr>
<td></td>
<td>move bit in DL</td>
</tr>
<tr>
<td></td>
<td>Shift and store the One bit in DL</td>
</tr>
<tr>
<td></td>
<td>Shift and store the most bit in DL</td>
</tr>
</tbody>
</table>
Simple CMP instruction uses _____ operation.

Select the correct option

- Addition
- Division
- Subtraction
- Multiplication
Question # 19 of 30 (Start time: 03:29:58 AM, 19 July 2020)

________ register is used to access the video memory.

Select the correct option

- ES
- AX
- AC
- BX
MC190204394: MUHAMMAD ASIF

CS401: Grand Quiz

Question # 20 of 30  (Start time: 03:30:12 AM, 19 July 2020)

Which of the following is the interrupt number for NMI?

Select the correct option

<table>
<thead>
<tr>
<th>Option</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. INT 0</td>
<td></td>
</tr>
<tr>
<td>2. INT 1</td>
<td></td>
</tr>
<tr>
<td>3. INT 2</td>
<td></td>
</tr>
<tr>
<td>4. INT 3</td>
<td></td>
</tr>
</tbody>
</table>
REPE repeat the following string instruction while the _______ is set.

Select the correct option

- Carry flag
- Zero flag
- Parity flag
- Direction flag
Question # 22 of 30 ( Start time: 03:31:15 AM, 19 July 2020 )
MOV [BX+SI+300], AX is a __________ addressing mode instruction.

Select the correct option

- Base + Index + Offset
- Base + Index
- Indexed Register Indirect + Offset
- Illegal
Question # 23 of 30 (Start time: 03:31:31 AM, 19 July 2020)

In a video memory, each screen location corresponds to _____ bytes.

Select the correct option

- [ ] 8
- [ ] 4
- [x] 2
- [ ] 1
Question # 24 of 30 (Start time: 03:32:36 AM, 19 July 2020)

The interrupt call loads new values in CS, IP and

Select the correct option

- DS
- SS
- FLAGS (√)
- BP
Question # 25 of 30 (Start time: 03:33:04 AM, 19 July 2020)

A memory pointer containing the address in a special area of memory called stack.

Select the correct option

- Stack Pointer
- Instruction Pointer
- Base Pointer
- Destination Pointer
Question # 26 of 30 (Start time: 03:33:20 AM, 19 July 2020)

The maximum parameters a subroutine can receive are______ when all the general registers are used.

Select the correct option

1. 7
2. 6
3. 5
4. 4
Question # 27 of 30 (Start time: 03:33:46 AM, 19 July 2020)

By default, CS is associated with

Select the correct option

- SS
- BP
- CX
- IP
**Question # 28 of 30  (Start time: 03:34:30 AM, 19 July 2020)**

MOVES is used to ＿＿＿＿＿＿ a block of memory.

Select the correct option

<table>
<thead>
<tr>
<th>Option</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Save</td>
</tr>
<tr>
<td></td>
<td>Move</td>
</tr>
<tr>
<td></td>
<td>Delete</td>
</tr>
<tr>
<td></td>
<td>Push</td>
</tr>
</tbody>
</table>
**Question # 29 of 30 ( Start time: 03:35:04 AM, 19 July 2020 )**

This jump is taken if the last arithmetic operation produced a positive number in its destination.

<table>
<thead>
<tr>
<th>Option</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>JNP</td>
<td></td>
</tr>
<tr>
<td>JO</td>
<td></td>
</tr>
<tr>
<td>JNS</td>
<td></td>
</tr>
<tr>
<td>JG</td>
<td></td>
</tr>
</tbody>
</table>
Question # 30 of 30 ( Start time: 03:35:39 AM, 19 July 2020 )

LESS instruction loads ____ register

Select the correct option

- ES
- DS
- AX
- DX
CS401: Grand Quiz

Question # 1 of 30 ( Start time: 03:38:43 AM, 19 July 2020 )

BH register is a _____ bit register.

Select the correct option

- 8
- 16
- 24
- 32
Which part of this (0000000000 B80500) encoded instruction is an offset?

Select the correct option:

- First Column
- Second column
- B8
- 0500
Question # 6 of 30 (Start time: 03:40:57 AM, 19 July 2020)

Which of the following instructions is used for non-destructive AND operation?

Select the correct option

- CMP
- NAND
- TEST
- ADC
**Question # 7 of 30 (Start time: 03:41:09 AM, 19 July 2020)**

A carry if generated and it dropped without being stored anywhere in the flag then this phenomenon is called ________________.

**Select the correct option**

- [ ] Paragraph boundaries
- [x] Address wraparound
- [ ] Overlapping segments
- [ ] Overflow
Question # 8 of 30 ( Start time: 03:41:39 AM, 19 July 2020 )

When the RET instruction is executed, it recovers the value of the ___________ from the stack.

Select the correct option

- Current instruction
- Previous instruction
- Accumulator register
- Instruction pointer
Question # 9 of 30  (Start time: 03:42:05 AM, 19 July 2020)

In case of 32-bit processor, the size of an accumulator register will be _____ bits.

Select the correct option

- 16
- 8
- 32
- 64
Question # 10 of 30 ( Start time: 03:42:16 AM, 19 July 2020 )

Which part of machine code tells the central processor to perform a certain task.

Select the correct option

- MOV
- SS
- Operation code
- CALL operation
Question # 11 of 30 (Start time: 03:42:31 AM, 19 July 2020)

In SCAS Example, We use SCASB with _____ and a zero in AL register to find a zero byte in a string.

Select the correct option

- REPNE
- SCAS
- MOV
- CALL
Question # 12 of 30 ( Start time: 03:42:57 AM, 19 July 2020 )

Whenever we need access to a memory location whose address is not known until run-time we use ________.

Select the correct option

- Destination Register
- Counter Register
- Accumulator Register
- Index Register
In the operation of _________ instruction, the most significant bit is copied to the carry flag and is inserted from the right, causing every bit to move one position to the left.

Select the correct option:

- Rotate Left (ROL)
- Rotate Through Carry Right (RCR)
- Rotate Through Carry Left (RCL)
- Rotate Right (ROR)
**Question # 15 of 30 ( Start time: 03:44:35 AM, 19 July 2020 )**

In multiplication algorithm, we take the first digit of the multiplier and multiply it with the ____________.

**Select the correct option**

<table>
<thead>
<tr>
<th>Option</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>☐</td>
<td>Multiplicand</td>
</tr>
<tr>
<td>☐</td>
<td>Reminder</td>
</tr>
<tr>
<td>☐</td>
<td>Divider</td>
</tr>
<tr>
<td>☐</td>
<td>Result</td>
</tr>
</tbody>
</table>
In assembly language, the first executable instruction of the code should be placed at this offset __________.

Select the correct option

- 0000
- 0100
- 0010
- 1100
Question # 17 of 30 (Start time: 03:45:40 AM, 19 July 2020)

BP stands for ________?

Select the correct option

- Bit pointer
- Basic pointer
- Base pointer
- Byte pointer
Question # 18 of 30  (Start time: 03:45:52 AM, 19 July 2020)
Which of the following is shifted right in the 4-bit multiplication algorithm?

Select the correct option
- Remainder
- Multiplier
- Multiplicand
- Modulus
Question # 19 of 30  (Start time: 03:46:57 AM, 19 July 2020)
What does the following instruction do?
Mov ax, 0xFFFF

Select the correct option

- Moving AX into 0xFFFF
- Store 0xFFFF into AX  **Correct Answer**
- Adding 0xFFFF into AX
- Copying 0xFFFF into AX
Question # 20 of 30  (Start time: 03:47:49 AM, 19 July 2020)

The other directive is "define word" or "dw" with the same syntax as "db" but reserving a whole word of ___ bits instead of a byte.

Select the correct option

- 32
- 8
- 16
- 64
Question # 21 of 30 (Start time: 03:48:07 AM, 19 July 2020)

Simple CMP instruction uses _____ operation.

Select the correct option

- Addition
- Division
- Subtraction
- Multiplication
**Question # 22 of 30 ( Start time: 03:48:20 AM, 19 July 2020 )**

________ also known as source operand since the data is moving to the stack from this operand.

<table>
<thead>
<tr>
<th>Select the correct option</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>PUSH</td>
<td>[ ]</td>
</tr>
<tr>
<td>POP</td>
<td>[ ]</td>
</tr>
<tr>
<td>CALL</td>
<td>[ ]</td>
</tr>
<tr>
<td>RET</td>
<td>[ ]</td>
</tr>
</tbody>
</table>
**Question # 23 of 30 ( Start time: 03:49:04 AM, 19 July 2020 )**

MUL instruction performs an unsigned multiplication of __________ with the source operand.

Select the correct option

- accumulator
- carry
- word
- base
Question # 24 of 30 (Start time: 03:49:56 AM, 19 July 2020)

When an element is pushed on the stack SP is decremented by __________.

Select the correct option

- 3
- 2
- 4
- 1
Question # 25 of 30  (Start time: 03:50:10 AM, 19 July 2020)

When divide overflow occurs, processor will be interrupted, this type of interrupt is called __________.

Select the correct option

- hardware interrupt
- software interrupt [*Correct answer*]
- processor exception
- logical interrupts
Question # 26 of 30 (Start time: 03:51:39 AM, 19 July 2020)

When a 32 bit number is divided by a 16 bit number, the remainder is of

Select the correct option

- 4 bits
- 8 bits
- 16 bits
- 32 bits

Waiting for quiz.vu.edu.pk...
The segment-offset pair is called a **segmentation**.
Question # 28 of 30 (Start time: 03:53:48 AM, 19 July 2020)

"mov [bp], al" moves the one byte contents of the AL register to the address contained in BP register in the current __________.

Select the correct option

- Stack Segment
- Data Segment
- Code Segment
- Extra Segment
CS401: Grand Quiz

Question # 29 of 30 (Start time: 03:55:14 AM, 19 July 2020)

Which of the following is a special instruction?

Select the correct option

- cmp ax, 0
- add bx, 0534
- mov ax, bx
- cli ; Clear the interrupt flag
Question # 30 of 30 (Start time: 03:55:47 AM, 19 July 2020)

IAPX86 architecture consists of

Select the correct option

- 10 Registers
- 14 Registers
- 16 Registers
- 32 Registers
Question # 2 of 30 ( Start time: 03:57:29 AM, 19 July 2020 )

The execution of the instruction "mov word [ES:160], 0x1230" will print a character on the screen at:

Select the correct option

- first column of second row
- second column of first row
- second column of second row
- first column of third row
MC190202527: MUHAMMAD RASHID MEHMOOD

CS401: Grand Quiz

Question # 3 of 30 (Start time: 03:58:26 AM, 19 July 2020)

After the execution of STOS, CX will be:

Select the correct option

- Incremented by 1
- Decremented by 1
- Decrement by 2
- Incremented by 2
Question # 4 of 30 (Start time: 03:59:44 AM, 19 July 2020)

________ is used for temporary diversion.

Select the correct option

- Call
- Jump
- Short jump
- Return
Which of the following is the interrupt number for NMI?

Select the correct option

- INT 0
- INT 1
- INT 2
- INT 3
CS401: Grand Quiz

Question # 6 of 30 ( Start time: 04:00:36 AM, 19 July 2020 )

After the execution of STOSWB, the CX will be:

Select the correct option

- Incremented by 1
- Decremented by 1
- Incremented by 2
- Decremented by 2
Question # 7 of 30 (Start time: 04:01:28 AM, 19 July 2020)

Our computers screen is like a 2-D array having rows and columns.

Select the correct option

- 25, 40
- 25, 80
- 80, 25
- 40, 25
Question # 8 of 30 (Start time: 04:02:06 AM, 19 July 2020)

The maximum parameters a subroutine can receive are _____ when all the general registers are used.

Select the correct option

- 7
- 6
- 5
- 4
Question # 10 of 30  ( Start time: 04:02:38 AM, 19 July 2020 )

Instruction makes the code reusable.

Select the correct option

<table>
<thead>
<tr>
<th>Option</th>
<th>Choice</th>
</tr>
</thead>
<tbody>
<tr>
<td>☐ Mov</td>
<td></td>
</tr>
<tr>
<td>☐ JMP</td>
<td></td>
</tr>
<tr>
<td>☑ CALL</td>
<td></td>
</tr>
<tr>
<td>☐ SHL</td>
<td></td>
</tr>
</tbody>
</table>
Question # 11 of 30  (Start time: 04:02:47 AM, 19 July 2020)
Simple CMP instruction uses _____ operation.

Select the correct option

- Addition
- Division
- Subtraction
- Multiplication
Question # 12 of 30 (Start time: 04:02:59 AM, 19 July 2020)

By default CS is associated with

Select the correct option

- SS
- BP
- CX
- IP

CONTACT FOR SOLVING QUIZ
03063548328
Question # 13 of 30 (Start time: 04:03:16 AM, 19 July 2020)

In 8088, there is a ____________ stack.

Select the correct option

- Incrementing
- Stagnant
- Decrementing
- Semi-automatic
Question # 14 of 30 (Start time: 04:03:49 AM, 19 July 2020)

Group of bits processor uses to inform memory which element to read/write is collectively known as

Select the correct option

<table>
<thead>
<tr>
<th>Option</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Control bus</td>
<td></td>
</tr>
<tr>
<td>Data bus</td>
<td></td>
</tr>
<tr>
<td>Address bus</td>
<td></td>
</tr>
<tr>
<td>RAM</td>
<td></td>
</tr>
</tbody>
</table>
Question # 15 of 30 (Start time: 04:04:42 AM, 19 July 2020)

DB-25 is a _______ port connector.

Select the correct option

- partially serial
- half duplex
- serial
- parallel
**Question # 16 of 30** (Start time: 04:04:55 AM, 19 July 2020)

DW can store ____ bit value in it.

<table>
<thead>
<tr>
<th>Option</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>8</td>
<td></td>
</tr>
<tr>
<td>16</td>
<td></td>
</tr>
<tr>
<td>24</td>
<td></td>
</tr>
<tr>
<td>32</td>
<td></td>
</tr>
</tbody>
</table>

Select the correct option
**Question # 17 of 30 ( Start time: 04:05:10 AM, 19 July 2020 )**

What does the following instruction do?
- `MOV AX, 0x4C00`
- `INT 0x21`

**Select the correct option**

1. Move number into ax registers
2. Terminate this program
3. Variables defining
4. Move ax into number
Question # 18 of 30 (Start time: 04:05:23 AM, 19 July 2020)

"mov [bx], ax" moves the two bytes contents of the AX register to the address contained in BX register in the current ________.

Select the correct option

- Stack Segment
- Data Segment
- Code Segment
- Extra Segment
Question # 19 of 30 (Start time: 04:05:37 AM, 19 July 2020)

Which of the following directive is used to reserve a 8 bit space in the memory for holding data?

Select the correct option

- db
- dw
- dd
- dq
Question # 20 of 30  (Start time: 04:05:49 AM, 19 July 2020)

is the basic means of storing temporary data on the stack.

Select the correct option

- PUSH
- POP
- CALL
- RET
Question # 21 of 30 ( Start time: 04:05:59 AM, 19 July 2020 )

CLI stands for?

Select the correct option

- Clear the interrupt flag
- Clock interrupts
- Cycle instruction
- Control instruction
**Question # 22 of 30 (Start time: 04:06:10 AM, 19 July 2020)**

BH register is a _____ bit register.

**Select the correct option**

- 8  
- 16  
- 24  
- 32  

CONTACT FOR SOLVING QUIZ

03063548328
CS401: Grand Quiz

Question # 24 of 30 (Start time: 04:07:52 AM, 19 July 2020)

To access the arguments from the stack, the immediate idea that strikes is to _____ them off the stack.

Select the correct option

- Push
- Pop
- Add
- Insert
Question # 25 of 30 (Start time: 04:08:23 AM, 19 July 2020)
When an element is pushed on the stack SP is decremented by _______ as the 8088 stack works on word sized elements.

Select the correct option

<p>| | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>One</td>
<td></td>
</tr>
<tr>
<td>Two</td>
<td></td>
</tr>
<tr>
<td>Three</td>
<td></td>
</tr>
<tr>
<td>Four</td>
<td></td>
</tr>
</tbody>
</table>
Question # 27 of 30 (Start time: 04:09:15 AM, 19 July 2020)

BP by default associated with

<table>
<thead>
<tr>
<th>Option</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CS</td>
<td></td>
</tr>
<tr>
<td>IP</td>
<td></td>
</tr>
<tr>
<td>SS</td>
<td></td>
</tr>
<tr>
<td>SP</td>
<td></td>
</tr>
</tbody>
</table>
Question # 28 of 30 (Start time: 04:10:18 AM, 19 July 2020)
Which part of this B80500 encoded instruction is an opcode?

<table>
<thead>
<tr>
<th>Select the correct option</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Opcode is 0500</td>
<td></td>
</tr>
<tr>
<td>Opcode is B80500</td>
<td></td>
</tr>
<tr>
<td>Opcode is B8</td>
<td></td>
</tr>
<tr>
<td>Opcode is B80</td>
<td></td>
</tr>
</tbody>
</table>
Question # 29 of 30 ( Start time: 04:10:39 AM, 19 July 2020 )

Data bus is ___________.

Select the correct option

- Uni-directional
- Bi-directional
- multi-directional
- None of the given
Question # 30 of 30 ( Start time: 04:11:00 AM, 19 July 2020 )

The correlation process from the interrupt number to the interrupt handler uses a table called ________.

Select the correct option

- Interrupt Calling Table
- Interrupt Handling Table
- Interrupt Vector Table
- Interrupt Correlation Table
Question # 1 of 30 (Start time: 04:22:48 AM, 18 July 2020)

Which of the following registers is used to hold address of the next instruction to be executed?

Select the correct option

- Memory address register
- Memory data register
- Instruction registers
- Program counter

CONTACT FOR SOLVING QUIZ

03063548328
Question # 2 of 30 (Start time: 04:23:42 AM, 18 July 2020)

CX register is mostly used as a

Select the correct option

- counter register
- flag register
- base register
- destination register
Question # 3 of 30  (Start time: 04:24:11 AM, 18 July 2020)

Which of the following operation is used to clear any specific bit in a binary number?

Select the correct option

- AND
- OR
- XOR
- NOT
Question # 4 of 30 ( Start time: 04:25:27 AM, 18 July 2020 )

Which part of this (0000000000 B80600) encoded instruction is an offset?

Select the correct option

- First Column
- Second column
- B8
- 0500
Question # 5 of 30 ( Start time: 04:26:39 AM, 18 July 2020 )

SCAS compares a source byte or word in register AL or AX with the __________ string element addressed by ES: DI and updates the flags.

Select the correct option

<table>
<thead>
<tr>
<th>Option</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>☐ Source</td>
<td></td>
</tr>
<tr>
<td>☑ Destination</td>
<td></td>
</tr>
<tr>
<td>☐ Flag</td>
<td></td>
</tr>
<tr>
<td>☐ Register</td>
<td></td>
</tr>
</tbody>
</table>
Question # 5 of 30 (Start time: 04:27:21 AM, 18 July 2020)

What does the given instruction do?

SHR DL, 1

Select the correct option

- Move right most bit in carry
- Move bit in DL
- Shift and store the One bit in DL
- Shift and store the most bit in DL
Question # 7 of 30 ( Start time: 04:28:06 AM, 18 July 2020 )

_________ is set only when the last mathematical or logical operation produces a zero in its destination.

Select the correct option

- Sign Flag
- Zero flag
- Trap Flag
- Parity Flag
Question # 8 of 30 (Start time: 04:28:36 AM, 18 July 2020)

The extension of assembly language file is

Select the correct option

- .doc
- .com
- .lst
- .asm
**CS401: Grand Quiz**

**Question # 9 of 30** (Start time: 04:28:51 AM, 18 July 2020)

Stack is a data structure that behaves in a first in last _____ manner.

<table>
<thead>
<tr>
<th>Option</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>In</td>
<td></td>
</tr>
<tr>
<td>Out</td>
<td></td>
</tr>
<tr>
<td>Push</td>
<td></td>
</tr>
<tr>
<td>Add</td>
<td></td>
</tr>
</tbody>
</table>
The instruction, MOV AX, 0005H belongs to the address mode of __________.

Select the correct option

- Register
- Direct
- Immediate
- Register Relative
Question # 11 of 30  (Start time: 04:29:27 AM, 18 July 2020)

This jump is taken if the last arithmetic operation produced a positive number in its destination.

Select the correct option:

- JNP
- JO
- JNS
- JG
Question # 12 of 30  (Start time: 04:30:43 AM, 18 July 2020)

BH register is a ____ bit register.

Select the correct option

- 8
- 16
- 24
- 32
The jump command that does not depend on the FLAG register is

Select the correct option

- JCXZ
- JO
- JNE
- JP
Question # 14 of 50 ( Start time: 04:32:20 AM, 18 July 2020 )

_______ is the basic means of storing temporary data on the stack.

Select the correct option

- PUSH
- POP
- CALL
- RET
**Question # 15 of 30 ( Start time: 04:32:42 AM, 18 July 2020 )**

_____ is a special instruction that load a segment register and a general purpose register from a memory locations.

**Select the correct option**

- [ ] SCAS
- [ ] MOV
- [ ] CALL
- [ ] LES
Which of the following addressing scheme has been used in the instruction MOV [BX], AX?

Select the correct option:

- Base Register Direct
- Base Register Indirect
- Base + Offset
- Base + Index
Question # 17 of 30 (Start time: 04:35:06 AM, 18 July 2020)

In a comparison, if the both operands are same. The result of subtraction will be zero and the zero flag will be _______.

Select the correct option

- Set
- Incremented
- Updated
- Decremented
Question # 19 of 30 (Start time: 04:36:45 AM, 18 July 2020)

________ and _______ are taken if the last arithmetic operation produced a number in its destination that has odd parity.

Select the correct option

- JP, JPE
- JS, JNS
- JCX, JNS
- JNP, JPO
Question # 20 of 30 (Start time: 04:37:44 AM, 18 July 2020)

In case of 32-bit processor, the size of an accumulator register will be _____ bits.

<table>
<thead>
<tr>
<th>Option</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>16</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td></td>
</tr>
<tr>
<td>32</td>
<td>✅</td>
</tr>
<tr>
<td>64</td>
<td></td>
</tr>
</tbody>
</table>
Question # 21 of 30 ( Start time: 04:38:52 AM, 18 July 2020 )

In branching mechanism, the assembler

Select the correct option

- Replaces the target with its address
- Does not replace until the test condition is satisfied
- Finds the Branch offset and replaces the Branch target with it
- Replaces the target with the value specified by the DATATRANS directive
Question # 22 of 30 ( Start time: 04:39:45 AM, 18 July 2020 )

Which of the following operation is used to set any specific bit in a binary number?

Select the correct option

- AND
- OR
- XOR
- NOT
Question # 23 of 30 (Start time: 04:40:31 AM, 18 July 2020)

The interrupt call loads new values in______ segment.

Select the correct option

- ES
- DS
- CS
- SS
Question # 24 of 30  (Start time: 04:41:14 AM, 18 July 2020)

The jump is taken if the last arithmetic operation changed the sign unexpectedly.

<table>
<thead>
<tr>
<th>Select the correct option</th>
</tr>
</thead>
<tbody>
<tr>
<td>JO</td>
</tr>
<tr>
<td>JNO</td>
</tr>
<tr>
<td>JNZ</td>
</tr>
<tr>
<td>JZ</td>
</tr>
</tbody>
</table>
Question # 25 of 30 ( Start time: 04:42:11 AM, 18 July 2020 )

All the addressing modes return the number after calculation, this number is known as_______.

Select the correct option

- Base Address
- Offset Address
- Data Address
- Effective Address
Question # 26 of 30 (Start time: 04:42:48 AM, 18 July 2020)
Conditional jumps can only ________.

Select the correct option

- far
- short
- near
- long
Question # 27 of 30 (Start time: 04:43:46 AM, 18 July 2020)

Which of the following instructions is used for non-destructive AND operation?

Select the correct option

- CMP
- NAND
- TEST
- ADC
Question # 28 of 30  (Start time: 04:44:30 AM, 18 July 2020)

Is a temporary storage places inside the processor.

Select the correct option

- Memory
- Ram
- Cache
- Register
There are just _____ block processing instructions in 8088.
**Question # 30 of 30 (Start time: 04:45:45 AM, 18 July 2020)**

MUL (multiply) instruction performs an unsigned multiplication of the source operand and the

<table>
<thead>
<tr>
<th>Select the correct option</th>
</tr>
</thead>
<tbody>
<tr>
<td>Accumulator</td>
</tr>
<tr>
<td>Carry</td>
</tr>
<tr>
<td>Word</td>
</tr>
<tr>
<td>Base</td>
</tr>
</tbody>
</table>