CS401 Assembly language
Grand Quiz Solved 100 % Confirm
(Covid-19)
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For Cs401 Students
Step by Step Topics of Computer Architecture & Assembly Language programming
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More Solved Quiz Will be share today inshaALLAH
Question # 1 of 30 (Start time: 04:22:48 AM, 18 July 2020)

Which of the following register is used to hold address of the next instruction to be executed?

Select the correct option

- Memory address register
- Memory data register
- Instruction registers
- Program counter
Question # 2 of 30 (Start time: 04:23:42 AM, 18 July 2020)

CX register is mostly used as a

Select the correct option

- counter register
- flag register
- base register
- destination register

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Question # 3 of 30 (Start time: 04:24:11 AM, 18 July 2020)

Which of the following operation is used to clear any specific bit in a binary number?

Select the correct option

- AND
- OR
- XOR
- NOT
Question # 4 of 30  ( Start time: 04:25:27 AM, 18 July 2020 )

Which part of this (0000000000 B80600) encoded instruction is an offset?

Select the correct option

- First Column
- Second column
- B8
- D500
Question # 5 of 30 (Start time: 04:26:39 AM, 18 July 2020)

SCAS compares a source byte or word in register AL or AX with the __________ string element addressed by ES: DI and updates the flags.

<table>
<thead>
<tr>
<th>Select the correct option</th>
</tr>
</thead>
<tbody>
<tr>
<td>Source</td>
</tr>
<tr>
<td>-</td>
</tr>
<tr>
<td>Destination</td>
</tr>
<tr>
<td>-</td>
</tr>
<tr>
<td>Flag</td>
</tr>
<tr>
<td>-</td>
</tr>
<tr>
<td>Register</td>
</tr>
<tr>
<td>-</td>
</tr>
</tbody>
</table>
Question # 6 of 30 (Start time: 04:22:21 AM, 18 July 2020)
What does the given instruction do?
SHR DL, 1

Select the correct option

- move right most bit in carry
- move bit in DL
- Shift and store the One bit in DL
- Shift and store the most bit in DL
Question # 7 of 30 (Start time: 04:28:06 AM, 18 July 2020)

_________ is set only when the last mathematical or logical operation produces a zero in its destination.

Select the correct option

- Sign Flag
- Zero flag
- Trap Flag
- Parity Flag
Question # 8 of 30 (Start time: 04:28:36 AM, 18 July 2020)
The extension of assembly language file is

Select the correct option

- .doc
- .com
- .lst
- .asm
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Question # 9 of 30  (Start time: 04:28:51 AM, 18 July 2020)

Stack is a data structure that behaves in a first in last ______ manner.

<table>
<thead>
<tr>
<th>Select the correct option</th>
</tr>
</thead>
<tbody>
<tr>
<td>In</td>
</tr>
<tr>
<td>Out</td>
</tr>
<tr>
<td>Push</td>
</tr>
<tr>
<td>Add</td>
</tr>
</tbody>
</table>

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Question #10 of 30 (Start time: 04:29:08 AM, 18 July 2020)

The instruction, MOV AX, 0005H belongs to the address mode of ___________.

Select the correct option

- Register
- Direct
- Immediate
- Register Relative
Question # 11 of 30  (Start time: 04:29:27 AM, 18 July 2020)

This jump is taken if the last arithmetic operation produced a positive number in its destination.

Select the correct option

- JNP
- JO
- JNS (Correct)
- JG
Question # 12 of 30 (Start time: 04:30:43 AM, 18 July 2020)

BH register is a ____ bit register.

Select the correct option

- 8
- 16
- 24
- 32
The jump command that does not depend on the flag register is

Select the correct option

- JCXZ
- JO
- JNE
- JP
Question # 14 of 30 (Start time: 04:32:20 AM, 16 July 2020)

is the basic means of storing temporary data on the stack.

Select the correct option

- PUSH
- POP
- CALL
- RET
Question # 15 of 30 (Start time: 04:32:42 AM, 18 July 2020)

______ is a special instruction that load a segment register and a general purpose register from a memory locations.

Select the correct option

<table>
<thead>
<tr>
<th>Option</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SCAS</td>
<td></td>
</tr>
<tr>
<td>MOV</td>
<td></td>
</tr>
<tr>
<td>CALL</td>
<td></td>
</tr>
<tr>
<td>LES</td>
<td></td>
</tr>
</tbody>
</table>
Question # 16 of 30  (Start time: 04:33:36 AM, 18 July 2020)

Which of the following addressing scheme has been used in the instruction MOV [BX], AX?

Select the correct option

- Base Register Direct
- Base Register Indirect
- Base + Offset
- Base + Index
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Question # 17 of 30 (Start time: 04:35:06 AM, 18 July 2020)

In a comparison, if the both operands are same. The result of subtraction will be zero and the zero flag will be ________.

Select the correct option

- Set
- Incremented
- Updated
- Decremented
Question # 19 of 30 (Start time: 04:36:45 AM, 18 July 2020)

_________ and _________ are taken if the last arithmetic operation produced a number in its destination that has odd parity.

Select the correct option

- JP, JPE
- JS, JNS
- JCX, JNS
- JNP, JPO
Question # 20 of 30 ( Start time: 04:37:44 AM, 18 July 2020 )

In case of 32-bit processor, the size of an accumulator register will be _____ bits.

Select the correct option

<table>
<thead>
<tr>
<th>Option</th>
<th>Answer</th>
</tr>
</thead>
<tbody>
<tr>
<td>16</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td></td>
</tr>
<tr>
<td>32</td>
<td>☑</td>
</tr>
<tr>
<td>64</td>
<td></td>
</tr>
</tbody>
</table>
Question # 21 of 30 (Start time: 04:38:52 AM, 18 July 2020)

In branching mechanism, the assembler

Select the correct option

- Replaces the target with its address
- Does not replace until the test condition is satisfied
- Finds the Branch offset and replaces the Branch target with it
- Replaces the target with the value specified by the DATABRARY directive
Question # 22 of 30 (Start time: 04:39:45 AM, 18 July 2020)

Which of the following operation is used to set any specific bit in a binary number?

Select the correct option

- AND
- OR
- XOR
- NOT
Question # 23 of 30 (Start time: 04:40:31 AM, 18 July 2020)

The interrupt call loads new values in ______ segment.

Select the correct option

- ES
- DS
- CS
- SS

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Question # 24 of 30 (Start time: 04:41:14 AM, 18 July 2020)

The jump is taken if the last arithmetic operation changed the sign unexpectedly.

Select the correct option

- JZ
- JNZ
- JNO
- JQ
Question # 25 of 30  (Start time: 04:42:11 AM, 18 July 2020)

All the addressing modes return the number after calculation, this number is known as________?

Select the correct option

- Base Address
- Offset Address
- Data Address
- Effective Address
Question # 26 of 30 ( Start time: 04:42:48 AM, 18 July 2020 )
Conditional jumps can only __________.

Select the correct option

- far
- short (Correct)
- near
- long
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Question # 27 of 30 (Start time: 04:43:46 AM, 18 July 2020)
Which of the following instructions is used for non-destructive AND operation?

Select the correct option

- [ ] CMP
- [ ] NAND
- [X] TEST
- [ ] ADC
Question # 28 of 30  (Start time: 04:44:30 AM, 18 July 2020)

is a temporary storage places inside the processor.

Select the correct option

- Memory
- Ram
- Cache
- Register
Question # 29 of 30  (Start time: 04:44:59 AM, 18 July 2020)

There are just ______ block processing instructions in 8088.

Select the correct option

- 6
- 5
- 4
- 3
Question # 30 of 30 (Start time: 04:45:45 AM, 18 July 2020)

MUL (multiply) instruction performs an unsigned multiplication of the source operand and the

Select the correct option

- Accumulator
- Carry
- Word
- Base