



### CS401 Assembly language

Grand Quiz Solved 100 % Confirm

(Covid-19)

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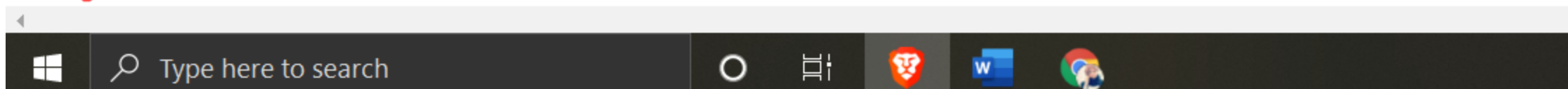


Question # 1 of 30 ( Start time: 04:22:48 AM, 18 July 2020 )

Which of the following register is used to hold address of the next instruction to be executed?

Select the correct option

- Memory address register
- Memory data register
- Instruction registers
- Program counter





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Question # 2 of 30 ( Start time: 04:23:42 AM, 18 July 2020 )

CX register is mostly used as a

Select the correct option

- counter register
- flag register
- base register
- desination register





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Question # 3 of 30 ( **Start time: 04:24:11 AM, 18 July 2020** )

Which of the following operation is used to clear any specific bit in a binary number?

Select the correct option

- |                                  |     |
|----------------------------------|-----|
| <input checked="" type="radio"/> | AND |
| <input type="radio"/>            | OR  |
| <input type="radio"/>            | XOR |
| <input type="radio"/>            | NOT |





**Question # 4 of 30 ( Start time: 04:25:27 AM, 18 July 2020 )**

Which part of this (00000000000 B80500) encoded instruction is an offset?

**Select the correct option**

- |                                  |               |
|----------------------------------|---------------|
| <input type="radio"/>            | First Column  |
| <input type="radio"/>            | Second column |
| <input type="radio"/>            | B8            |
| <input checked="" type="radio"/> | 0500          |





Question # 5 of 30 ( Start time: 04:26:39 AM, 18 July 2020 )

SCAS compares a source byte or word in register AL or AX with the \_\_\_\_\_ string element addressed by ES: DI and updates the flags.

Select the correct option

- Source
- Destination
- Flag
- Register





**Question # 6 of 30 ( Start time: 04:27:21 AM, 18 July 2020 )**

What does the given instruction do?  
SHR DL, 1

**Select the correct option**

- |                                  |                                    |
|----------------------------------|------------------------------------|
| <input type="radio"/>            | move right most bit in carry       |
| <input type="radio"/>            | move bit in DL                     |
| <input checked="" type="radio"/> | Shift and store the One bit in DL  |
| <input type="radio"/>            | Shift and store the most bit in DL |





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Question # 7 of 30 ( Start time: 04:28:06 AM, 18 July 2020 )

\_\_\_\_\_ is set only when the last mathematical or logical operation produces a zero in its destination.

Select the correct option

- Sign Flag
- Zero flag
- Trap Flag
- Parity Flag







Question # 8 of 30 ( Start time: 04:28:36 AM, 18 July 2020 )

The extension of assembly language file is

Select the correct option

- .doc
- .com
- .lst
- .asm





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Question # 9 of 30 ( Start time: 04:28:51 AM, 18 July 2020 )

Stack is a data structure that behaves in a first in last \_\_\_\_\_ manner.

Select the correct option

- In
- Out
- Push
- Add





**Question # 10 of 30 ( Start time: 04:29:08 AM, 18 July 2020 )**

The instruction, MOV AX, 0005H belongs to the address mode of \_\_\_\_\_.

**Select the correct option**

- Register
- Direct
- Immediate
- Register Relative





**Question # 11 of 30 ( Start time: 04:29:27 AM, 18 July 2020 )**

This jump is taken if the last arithmetic operation produced a positive number in its destination.

Select the correct option

- |                                  |     |
|----------------------------------|-----|
| <input type="radio"/>            | JNP |
| <input type="radio"/>            | JO  |
| <input checked="" type="radio"/> | JNS |
| <input type="radio"/>            | JG  |





Question # 12 of 30 ( Start time: 04:30:43 AM, 18 July 2020 )

BH register is a \_\_\_\_\_ bit register,

Select the correct option

- 8
- 16
- 24
- 32





The Jump command that does not depend on FLAG register is

Select the correct option

- |                                  |      |
|----------------------------------|------|
| <input checked="" type="radio"/> | JCZX |
| <input type="radio"/>            | JO   |
| <input type="radio"/>            | JNE  |
| <input type="radio"/>            | JP   |





QUESTION 7 14 OF 30 ( START TIME: 04:52:20 AM, 18 JULY 2020 )

\_\_\_\_\_ is the basic means of storing temporary data on the stack.

Select the correct option

- |                                  |      |
|----------------------------------|------|
| <input checked="" type="radio"/> | PUSH |
| <input type="radio"/>            | POP  |
| <input type="radio"/>            | CALL |
| <input type="radio"/>            | RET  |





Question # 15 of 30 ( Start time: 04:32:42 AM, 18 July 2020 )

\_\_\_\_\_ is a special instruction that load a segment register and a general purpose register from a memory locations.

Select the correct option

- SCAS
- MOV
- CALL
- LES







Question # 16 of 30 ( Start time: 04:33:36 AM, 18 July 2020 )

Which of the following addressing scheme has been used in the instruction MOV [BX], AX?

Select the correct option

- Base Register Direct
- Base Register Indirect
- Base + Offset
- Base + Index





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Question # 17 of 30 ( Start time: 04:35:06 AM, 18 July 2020 )

In a comparison, if the both operands are same. The result of subtraction will be zero and the zero flag will be \_\_\_\_\_.

Select the correct option

- |                                  |             |
|----------------------------------|-------------|
| <input checked="" type="radio"/> | Set         |
| <input type="radio"/>            | Incremented |
| <input type="radio"/>            | Updated     |
| <input type="radio"/>            | Decrementd  |





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Quiz Start Time:

Question # 19 of 30 ( Start time: 04:36:45 AM, 18 July 2020 )

\_\_\_\_\_ and \_\_\_\_\_ are taken if the last arithmetic operation produced a number in its destination that has odd parity.

Select the correct option

- |                                  |          |
|----------------------------------|----------|
| <input type="radio"/>            | JP, JPE  |
| <input type="radio"/>            | JS, JNS  |
| <input type="radio"/>            | JCX, JNS |
| <input checked="" type="radio"/> | JNP, JPO |





Question # 20 of 30 ( Start time: 04:37:44 AM, 18 July 2020 )

In case of 32-bit processor, the size of an accumulator register will be \_\_\_\_\_ bits.

Select the correct option

- 16
- 8
- 32
- 64





Question # 21 of 30 ( Start time: 04:38:52 AM, 18 July 2020 )

In branching mechanism, the assembler

Select the correct option

- Replaces the target with its address
- Does not replace until the test condition is satisfied
- Finds the Branch offset and replaces the Branch target with it
- Replaces the target with the value specified by the DATAWORD directive





Question # 22 of 30 ( Start time: 04:39:45 AM, 18 July 2020 )

Which of the following operation is used to set any specific bit in a binary number?

Select the correct option

- |                                  |     |
|----------------------------------|-----|
| <input type="radio"/>            | AND |
| <input checked="" type="radio"/> | OR  |
| <input type="radio"/>            | XOR |
| <input type="radio"/>            | NOT |





**Question # 23 of 30 ( Start time: 04:40:31 AM, 18 July 2020 )**

The interrupt call loads new values in\_\_\_\_\_ segment.

Select the correct option

- |                                  |    |
|----------------------------------|----|
| <input type="radio"/>            | ES |
| <input type="radio"/>            | DS |
| <input checked="" type="radio"/> | CS |
| <input type="radio"/>            | SS |





Question # 24 of 30 ( Start time: 04:41:14 AM, 18 July 2020 )

The jump is taken if the last arithmetic operation changed the sign unexpectedly.

Select the correct option

- |                                  |     |
|----------------------------------|-----|
| <input checked="" type="radio"/> | JO  |
| <input type="radio"/>            | JNO |
| <input type="radio"/>            | JNZ |
| <input type="radio"/>            | JZ  |







**Question # 25 of 30 ( Start time: 04:42:11 AM, 18 July 2020 )**

All the addressing modes return the number after calculation, this number is known as \_\_\_\_\_?

**Select the correct option**

- Base Address
- Offset Address
- Data Address
- Effective Address





Question # 26 of 30 ( Start time: 04:42:48 AM, 18 July 2020 )

Conditional jumps can only \_\_\_\_\_.

Select the correct option

- far
- short
- near
- long





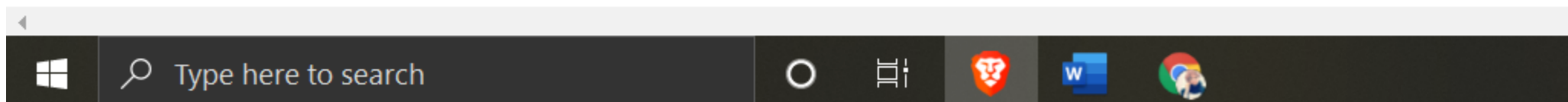
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Question # 27 of 30 ( Start time: 04:43:46 AM, 18 July 2020 )

Which of the following instructions is used for non-destructive AND operation?

Select the correct option

- CMP
- NAND
- TEST
- ADC



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Question # 28 of 30 ( Start time: 04:44:30 AM, 18 July 2020 )

\_\_\_\_\_ is a temporary storage places inside the processor.

Select the correct option

- Memory
- Ram
- Cache
- Register





Question # 29 of 30 ( Start time: 04:44:59 AM, 18 July 2020 )

There are just \_\_\_\_\_ block processing instructions in 8088.

Select the correct option

- 6
- 5
- 4
- 3





Question # 30 of 30 ( Start time: 04:45:45 AM, 18 July 2020 )

MUL (multiply) Instruction performs an unsigned multiplication of the source operand and the \_\_\_\_\_.

Select the correct option

- Accumulator
- Carry
- Word
- Base

